

Compal Confidential

PEW71_81_91 UMA <LA-6582P> M/B Schematics Document

Intel Arrandale Processor with DDRIII + Ibex Peak-M

2010-07-09

REV:1.0

Acer Aspire 5742
Acer Aspire 5733-4851

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Issued Date	2009/5/12	Deciphered Date	2010/04/15	Title	SCHEMATIC,MB A6582
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				Date:	Friday, August 27, 2010
				Sheet	1 of 48

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Model Name PEW71_81_91 UMA

File Name : LA-6582P

ZZZ1

ZZZ2

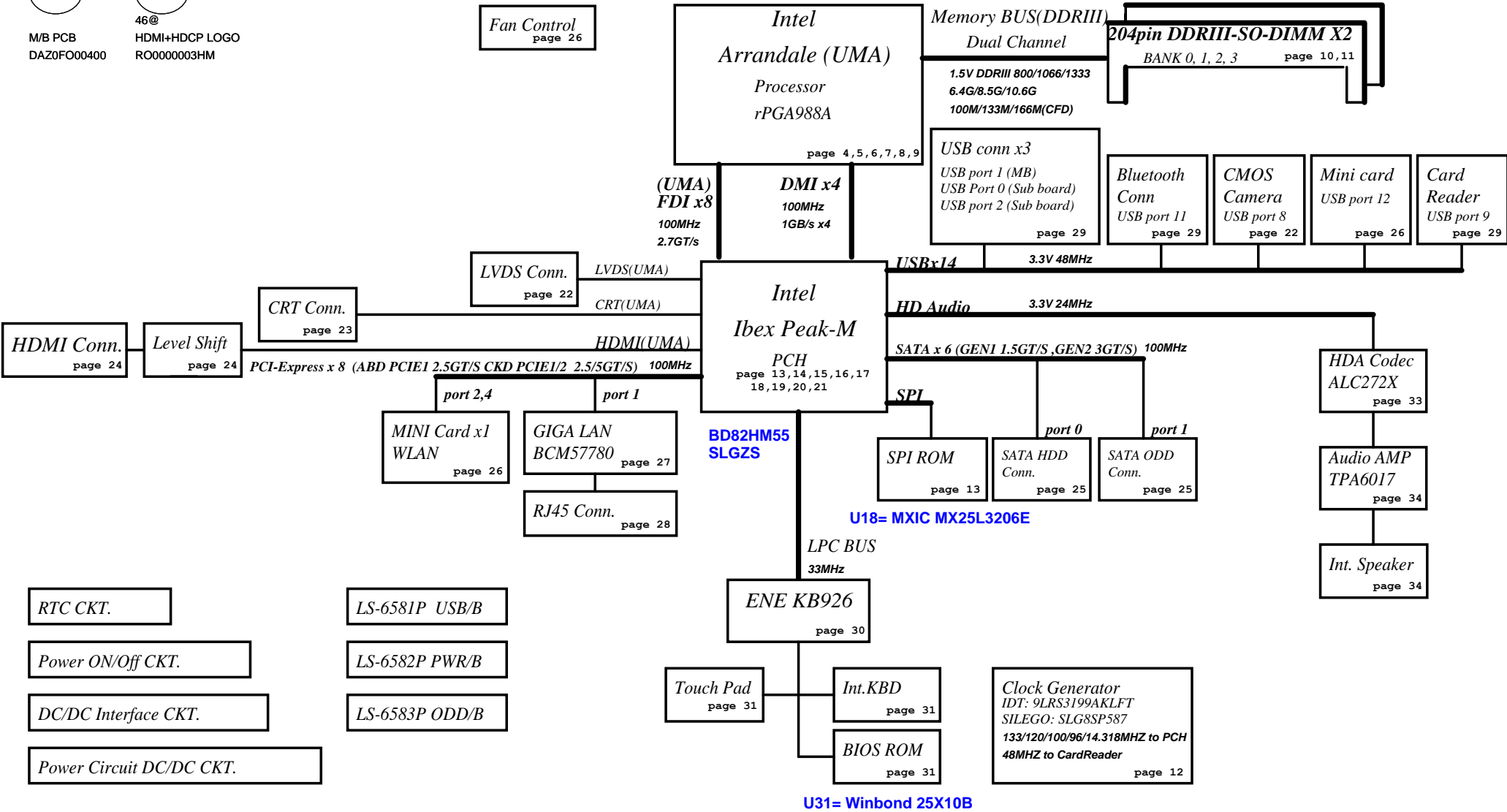
46@

M/B PCB

DAZ0FO00400

HDMI+HDCP LOGO

RO0000003HM



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				Rev	B

Power Plane	Description	S1	S3	S5
VIN	Adapter power supply (19V)	N/A	N/A	N/A
B+	AC or battery power rail for power circuit.	N/A	N/A	N/A
+CPU_CORE	Core voltage for CPU	ON	ON	OFF
+0.75VS	0.75V switched power rail for DDR terminator	ON	OFF	OFF
+1.05VS	1.05V switched power rail for PCH	ON	OFF	OFF
+1.05VS_VTT	1.05V switched power rail (1.05 for AUB CPU)	ON	OFF	OFF
+1.5V	1.5V power rail for DDRIII	ON	ON	OFF
+1.5VS	1.5V switched power rail	ON	OFF	OFF
+1.8VS	1.8V switched power rail	ON	OFF	OFF
+3VALW	3.3V always on power rail	ON	ON	ON*
+3V_LAN	3.3V power rail for LAN	ON	ON	ON*
+3VS	3.3V switched power rail	ON	OFF	OFF
+5VALW	5V always on power rail	ON	ON	ON*
+5VS	5V switched power rail	ON	OFF	OFF
+VSB	VSB always on power rail	ON	ON	ON*
+RTCVCC	RTC power	ON	ON	ON

External PCI Devices

EC SM Bus1 address

EC SM Bus2 address

Ibex SM Bus address

Device	Address
Clock Generator (9LRS3199AKLFT, SLG8SP587)	1101 0010b
DDR DIMM0	1001 000Xb
DDR DIMM2	1001 010Xb

Project ID / Board ID Table for EC-AD channel

Vcc	3.3V +/- 5%					
Ra/Rc	100K +/- 5%					
	Rb / Rd	VAD_BID min	VAD_BID typ	VAD_BID max	Board ID	Project ID
0	0	0 V	0 V	0 V	0.1	Original NEW70/80/90/50/71/91
1	8.2K +/- 5%	0.216 V	0.250 V	0.289 V	0.2	PEW71/81/91 Audio Mono/Crystal
2	18K +/- 5%	0.436 V	0.503 V	0.538 V	0.3	
3	33K +/- 5%	0.712 V	0.819 V	0.875 V	1.0	
4	56K +/- 5%	1.036 V	1.185 V	1.264 V		
5	100K +/- 5%	1.453 V	1.650 V	1.759 V		
6	200K +/- 5%	1.935 V	2.200 V	2.341 V		PEW71/81/91 Audio Mono/SUSCLK
7	NC	2.500 V	3.300 V	3.300 V		NEW71/91 Optumis

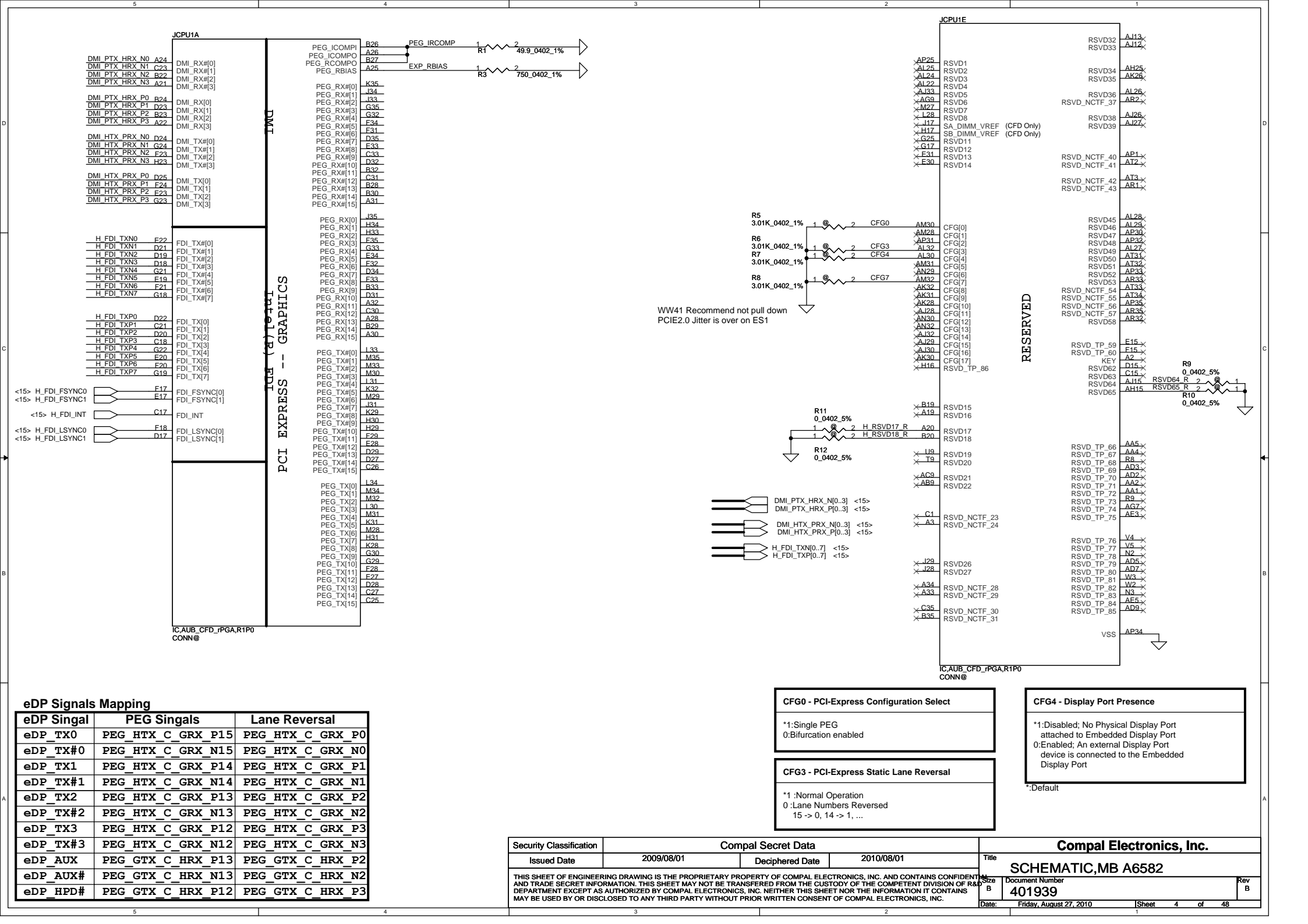
BTO Option Table

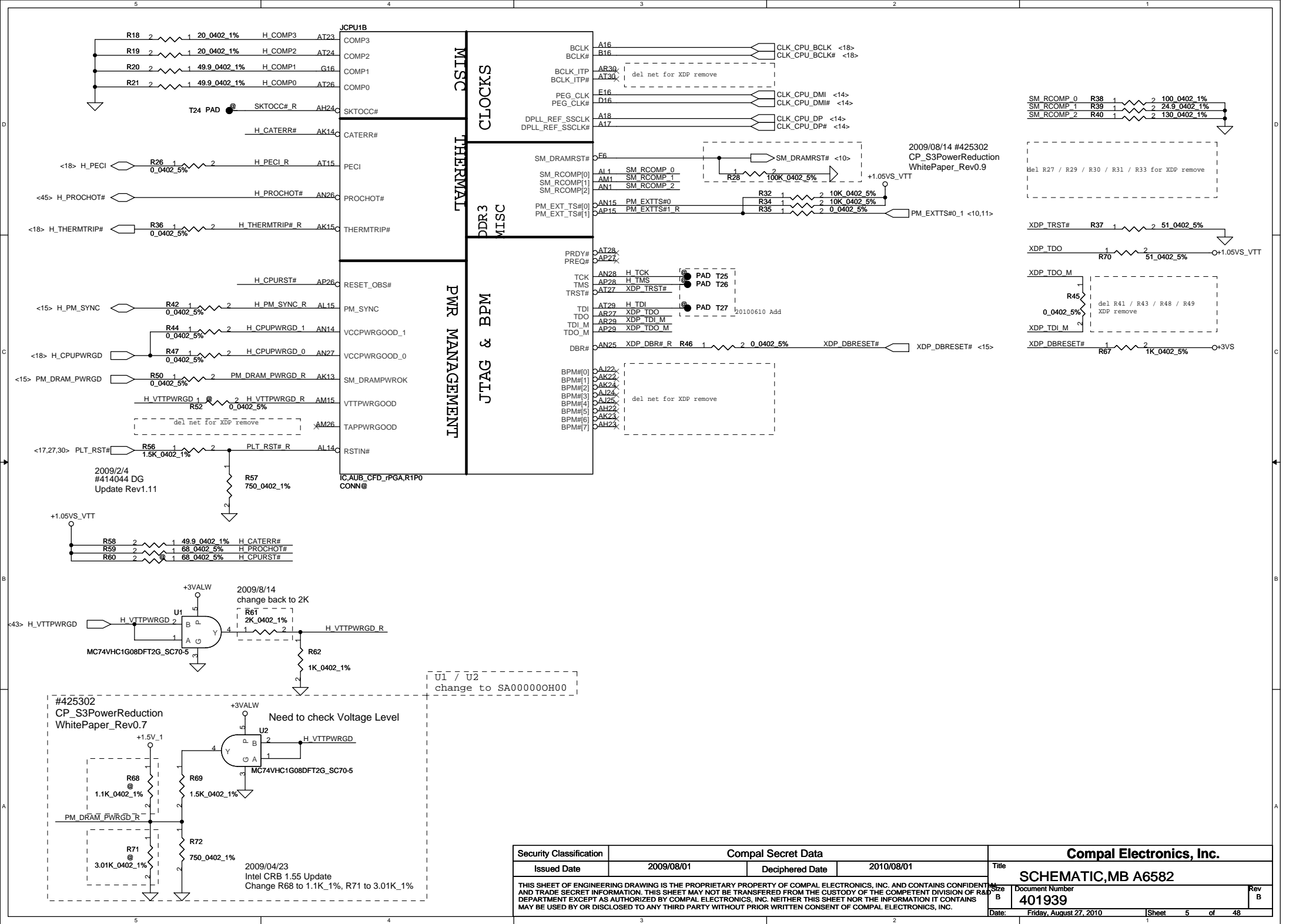
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USB Port Table

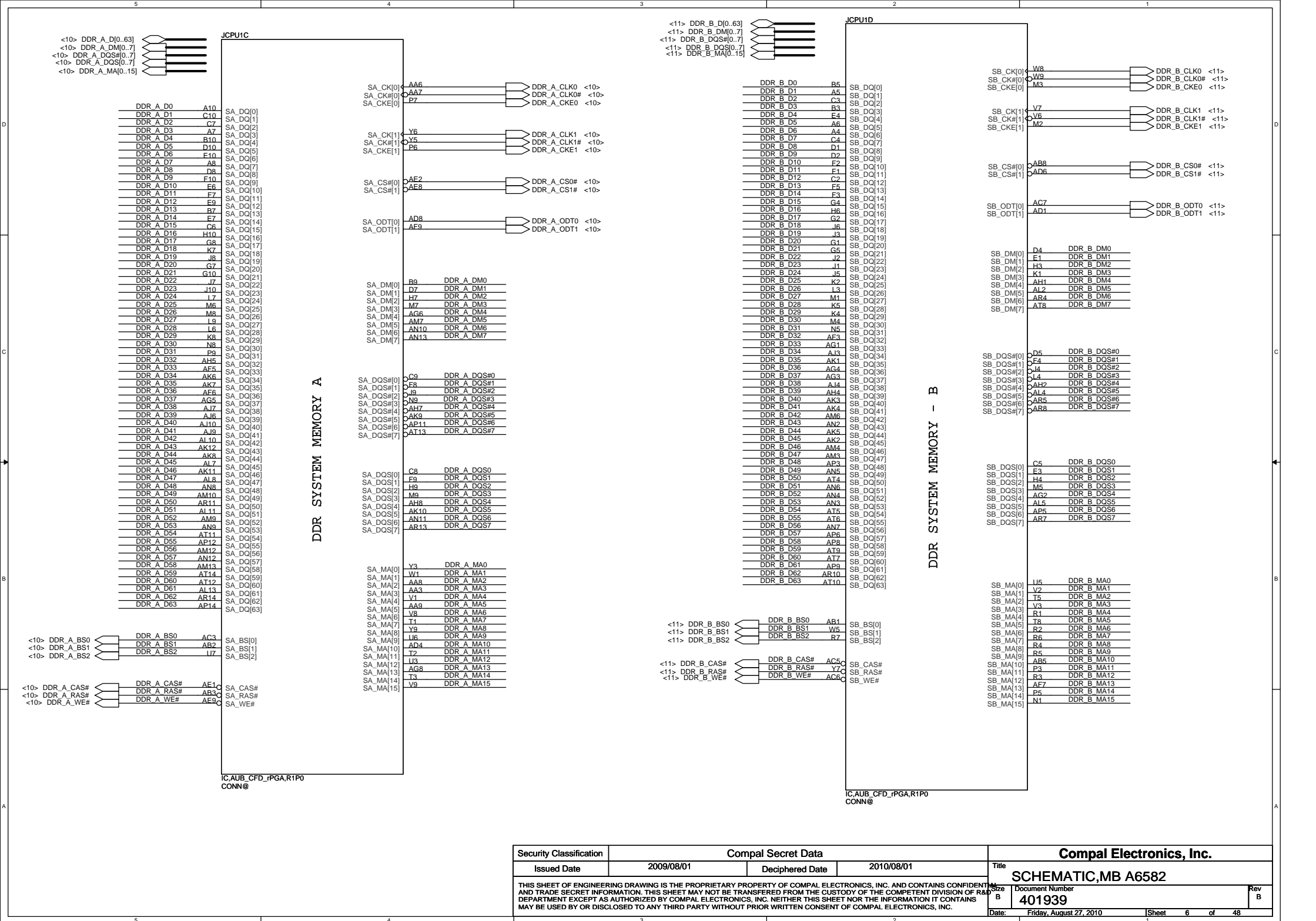
USB 2.0	USB 1.1	Port	4 External USB Port	3 External USB Port	
EHCI1	UHCI0	0	Ext1 Left Low USB	Ext1 Left Low USB	
		1	Ext2 Left High USB	Ext2 Left High USB	
		2	Ext3 Right USB	Ext3 Right USB	
	UHCI1	3			
		UHCI2	4		
			5		
	UHCI3	6			
		7			
EHCI2	UHCI4	8	Camera	Camera	
		9	Card Reader	Card Reader	
	UHCI5	10			
		11	Blue Tooth	Blue Tooth	
	UHCI6	12	1st Min-Card	1st Min-Card	
		13			

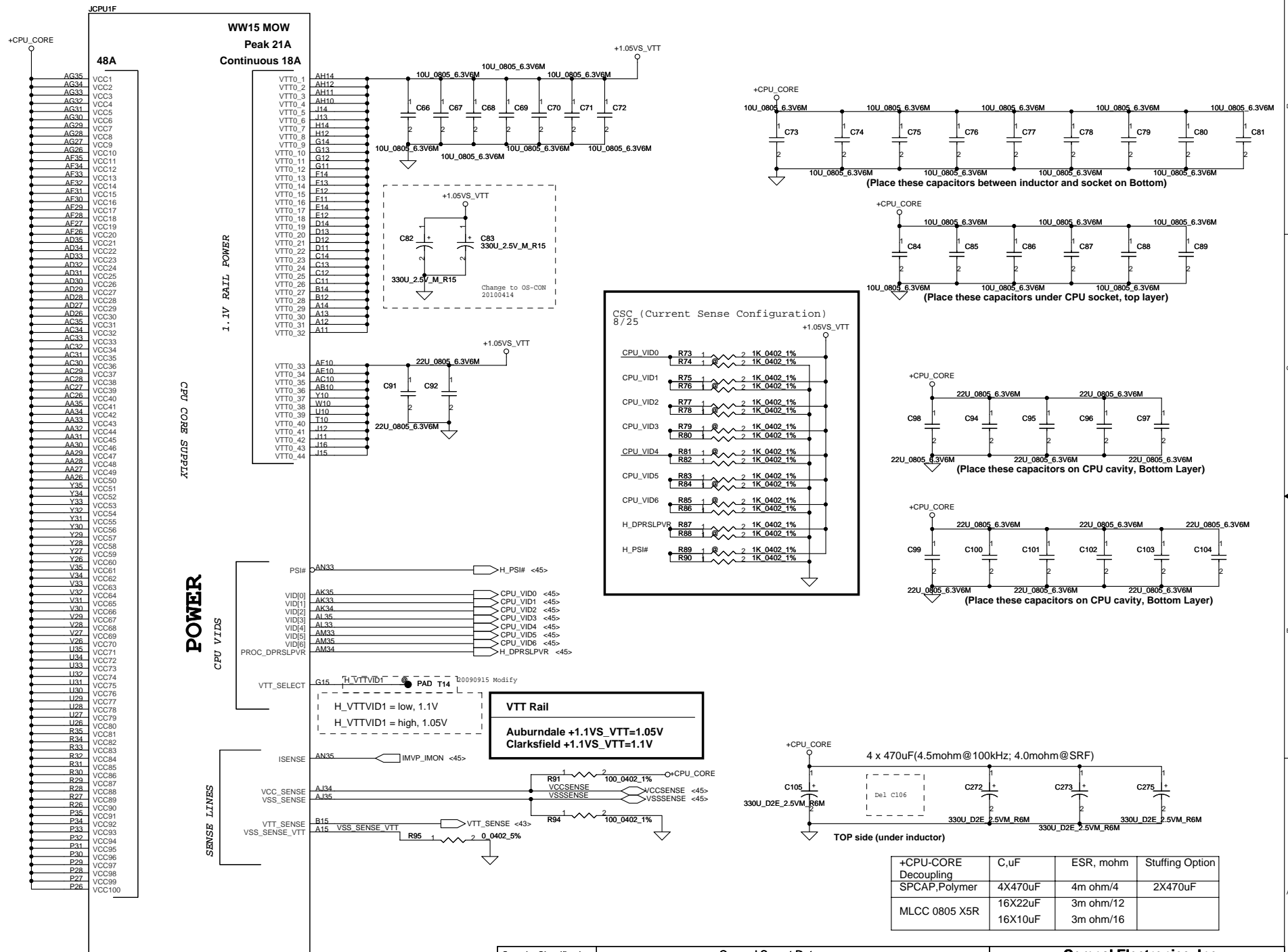
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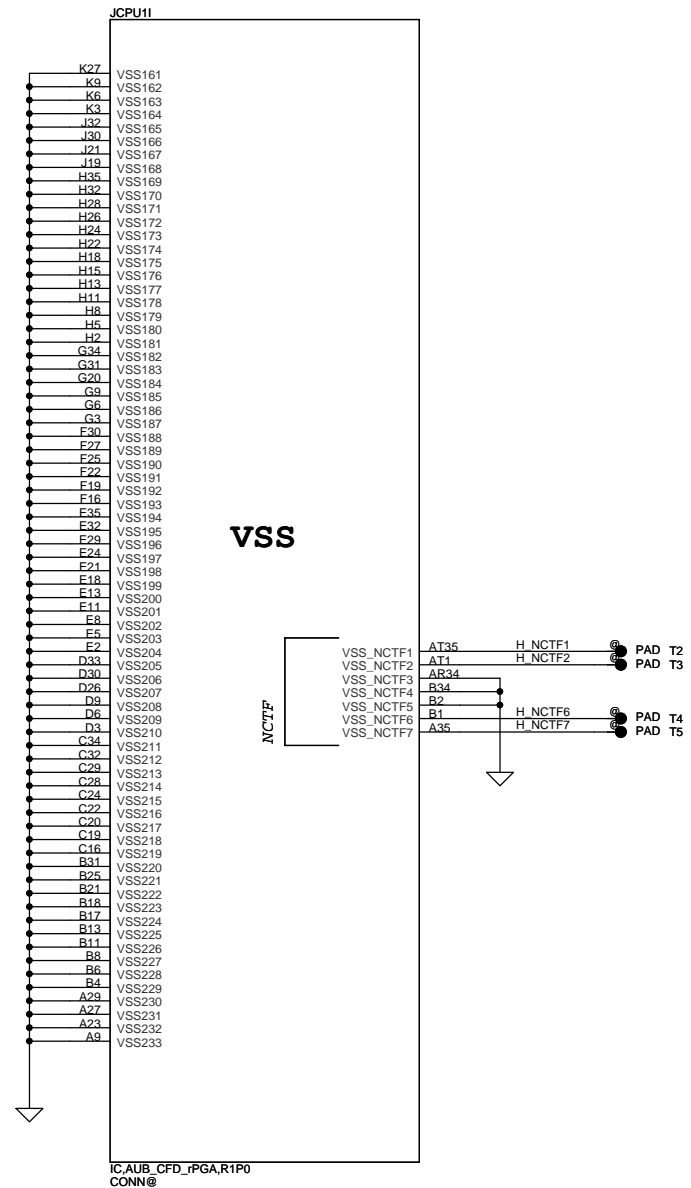
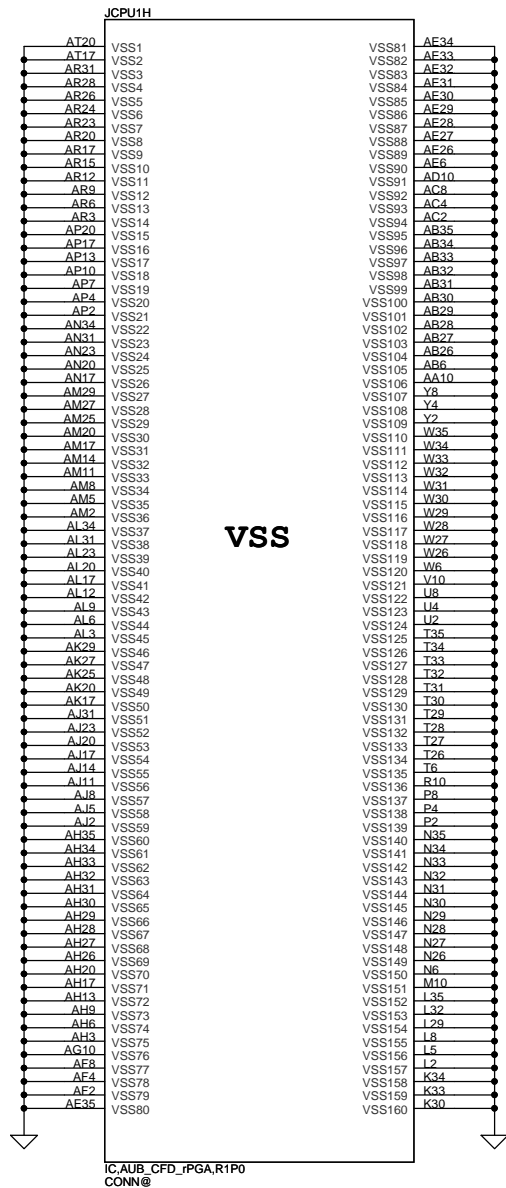
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								Date:		Friday, August 27, 2010	



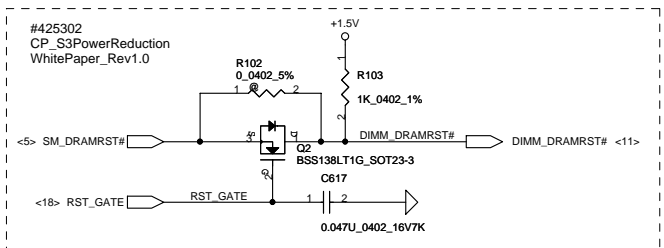
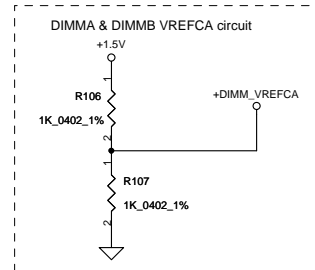
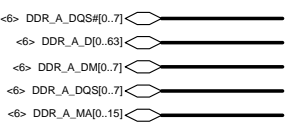
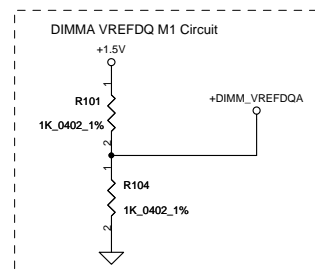


IC,AUB_CFD_PGA,R1P0
CONN@

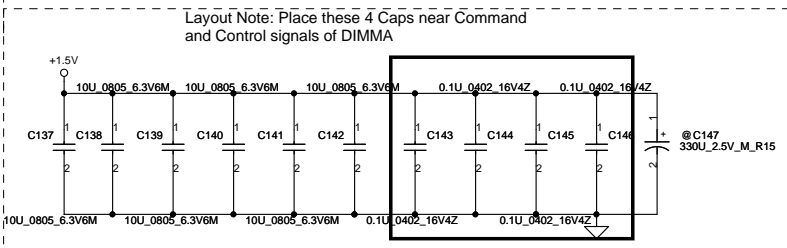
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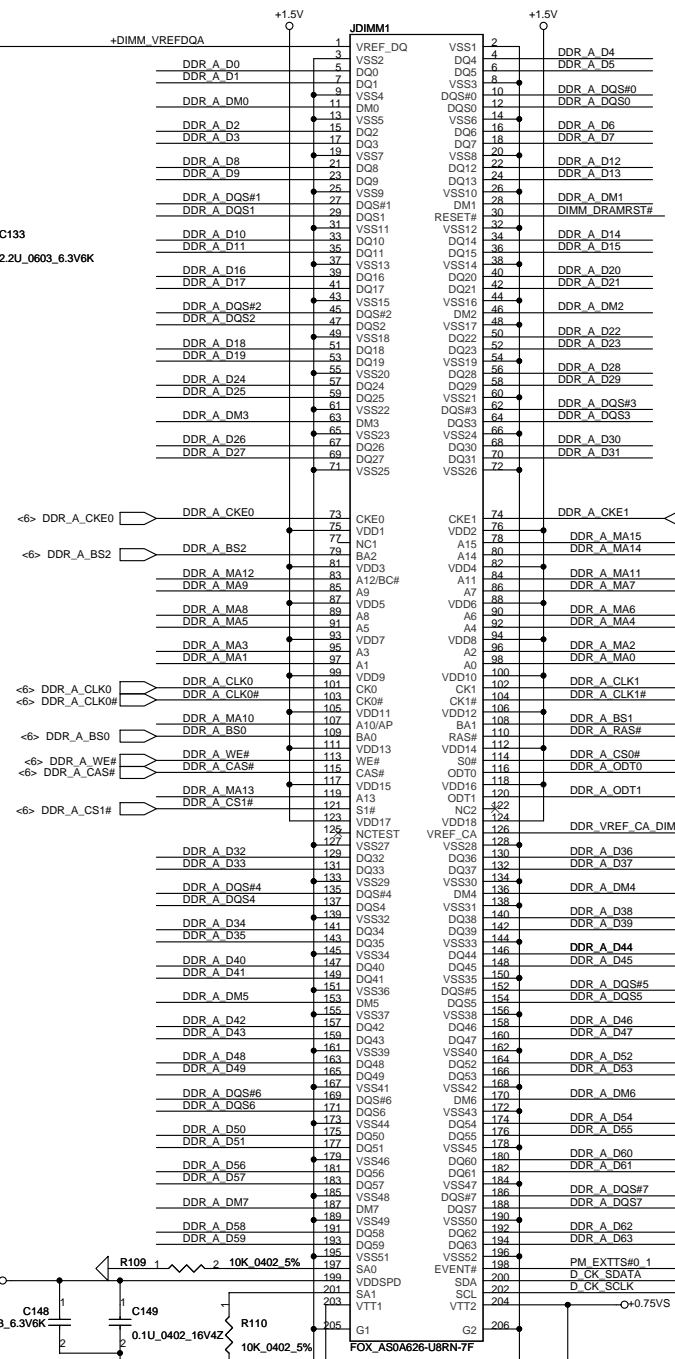
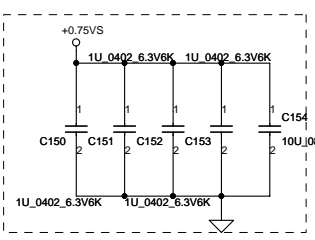
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Layout Note:
Place near JDIMM1



Layout Note:
Place near JDIMM1.203 & JDIMM1.204

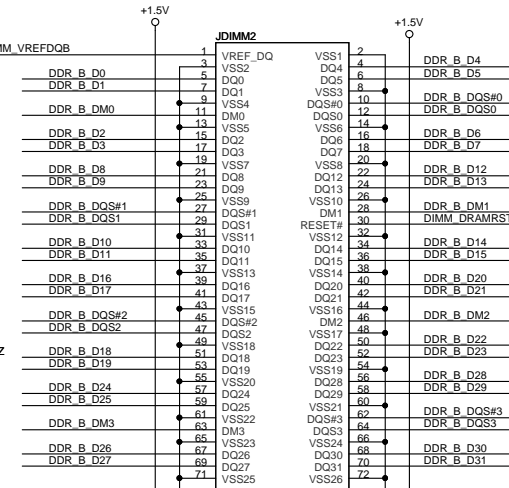
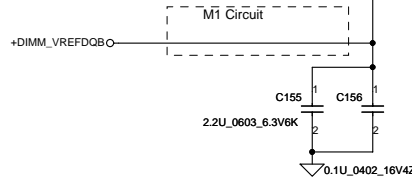
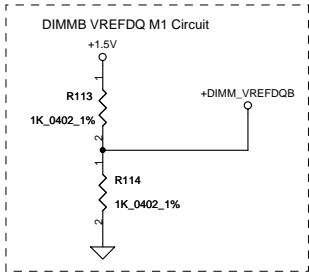


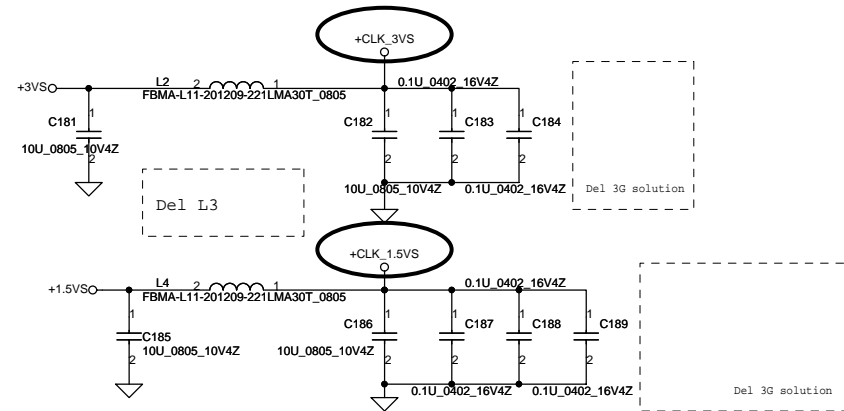
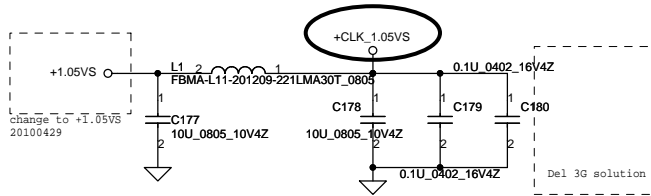
DDR3 SO-DIMM A
Change to Reverse Type
8mm High

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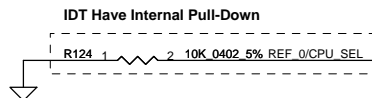
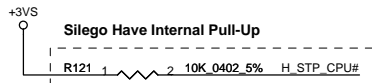
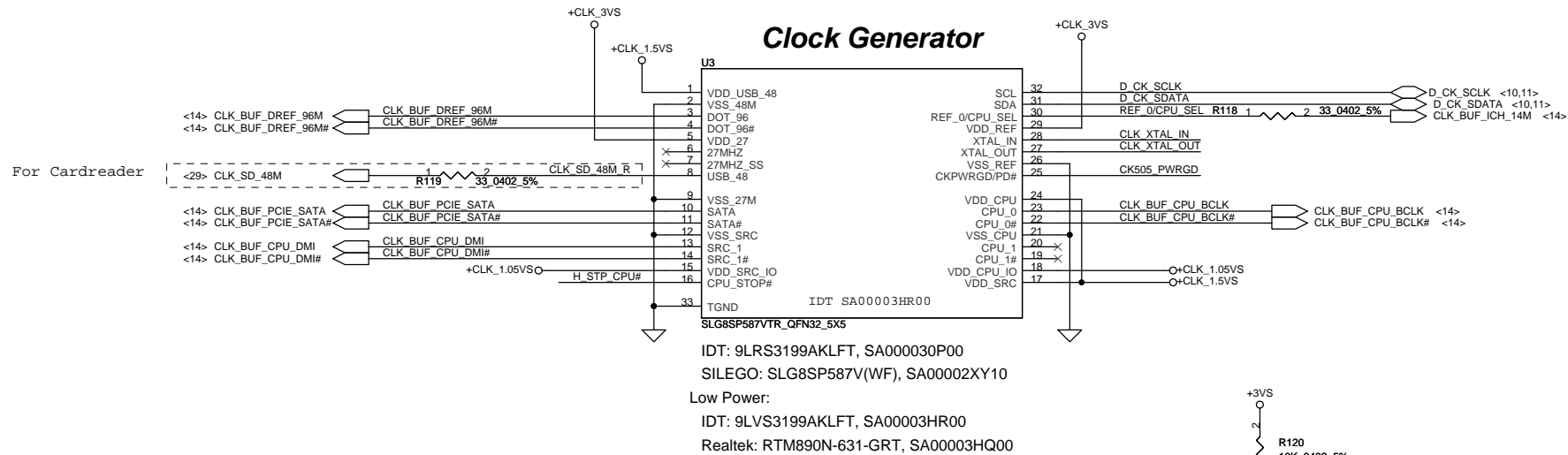
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<6> DDR_B_D[0..63]
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2008/9/8 #400755
Calpella Clarksfield
DDR3 SO-DIMM
VREFDQ Platform
Design Guide Change Details

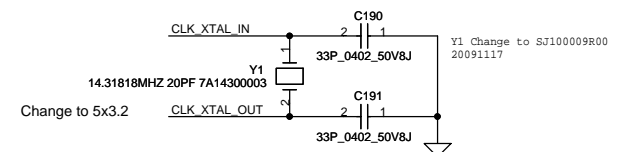
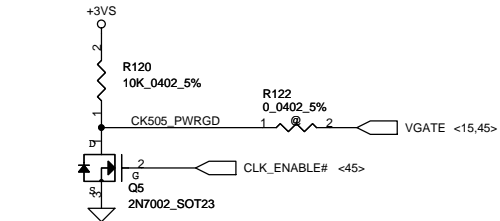
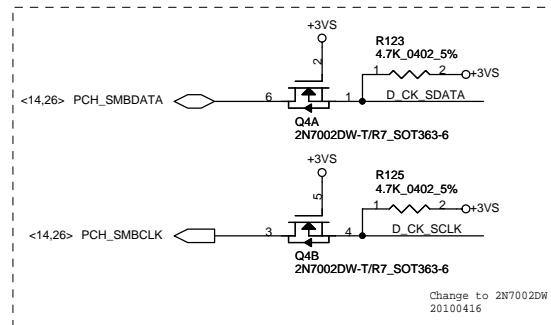




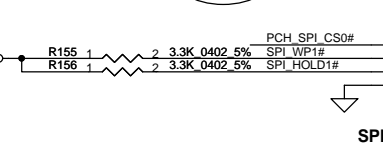
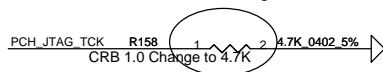
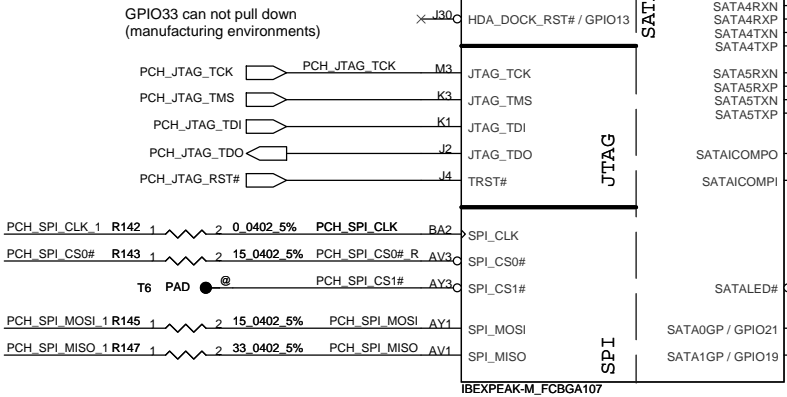
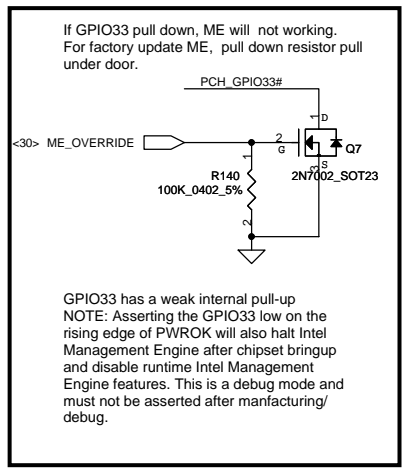
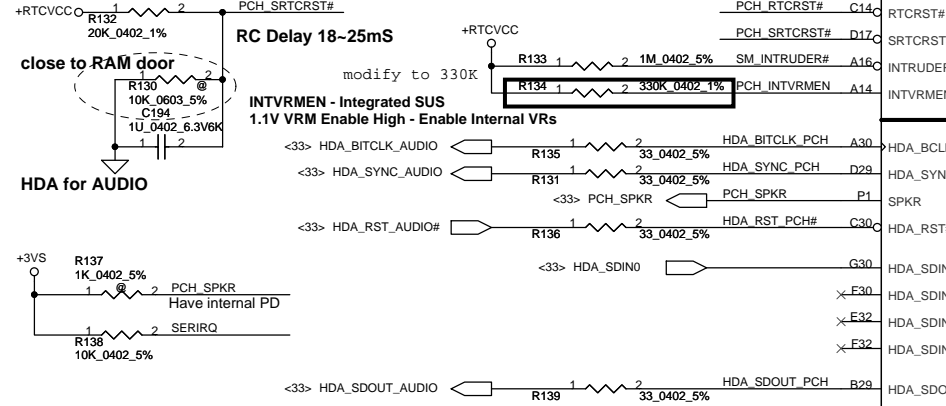
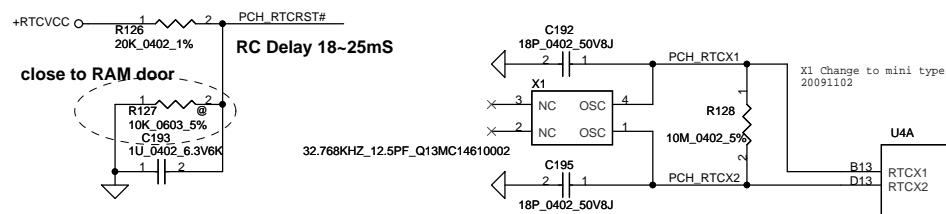
Clock Generator



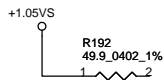
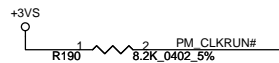
PIN 30	CPU_0	CPU_1
0 (Default)	133MHz	133MHz
1	100MHz	100MHz



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				Date:		Friday, August 27, 2010		Sheet 12 of 48	



<4> DMI_HTX_PRX_N[0..3] DMI HTX PRX_N[0..3]
<4> DMI_HTX_PRX_P[0..3] DMI HTX PRX_P[0..3]
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<4> DMI_PTX_HRX_P[0..3] DMI PTX HRX_P[0..3]



R195 Change to 10K for WW37
20090916

U4C
DMI HTX PRX N0 BC24
DMI HTX PRX N1 BJ22
DMI HTX PRX N2 AW20
DMI HTX PRX N3 BJ20
DMI HTX PRX P0 BD24
DMI HTX PRX P1 BG22
DMI HTX PRX P2 BA20
DMI HTX PRX P3 BG20
DMI PTX HRX N0 BE22
DMI PTX HRX N1 BF21
DMI PTX HRX N2 BD20
DMI PTX HRX N3 BE18
DMI PTX HRX P0 BD22
DMI PTX HRX P1 BH21
DMI PTX HRX P2 BC20
DMI PTX HRX P3 BD18

DMIORXN
DMI1RXN
DMI2RXN
DMI3RXN
DMIORXP
DMI1RXP
DMI2RXP
DMI3RXP
DMIOTXN
DMI1TXN
DMI2TXN
DMI3TXN
DMIOTXP
DMI1TXP
DMI2TXP
DMI3TXP

REV1.0

FDI_RXN0 BA18 H_FDI_TXN0
FDI_RXN1 BH17 H_FDI_TXN1
FDI_RXN2 BD16 H_FDI_TXN2
FDI_RXN3 BJ16 H_FDI_TXN3
FDI_RXN4 BA16 H_FDI_TXN4
FDI_RXN5 BE14 H_FDI_TXN5
FDI_RXN6 BA14 H_FDI_TXN6
FDI_RXN7 BC12 H_FDI_TXN7
FDI_RXP0 BB18 H_FDI_TXP0
FDI_RXP1 BE17 H_FDI_TXP1
FDI_RXP2 BC16 H_FDI_TXP2
FDI_RXP3 BG16 H_FDI_TXP3
FDI_RXP4 AW16 H_FDI_TXP4
FDI_RXP5 BD14 H_FDI_TXP5
FDI_RXP6 BB14 H_FDI_TXP6
FDI_RXP7 BD12 H_FDI_TXP7

FDI_INT BJ14 H_FDI_INT <4>
FDI_FSYNC0 BE13 H_FDI_FSYNC0 <4>
FDI_FSYNC1 BH13 H_FDI_FSYNC1 <4>
FDI_LSYNC0 BJ12 H_FDI_LSYNC0 <4>
FDI_LSYNC1 BG14 H_FDI_LSYNC1 <4>

H_FDI_TXN[0..7] H_FDI_TXN[0..7] <4>
H_FDI_TXP[0..7] H_FDI_TXP[0..7] <4>

<5> XDP_DBRESET# XDP_DBRESET# T6C
SYS_PWROK R197 2 1 0 0402_5% SYS_PWROK R M6
VGATE R198 2 1 0 0402_5%
SYS_PWROK R B17
MEPWROK K5
LAN_RST# A10C
LAN_RST#
<5> PM_DRAM_PWRGD D9C
DRAMPWROK
PCH_RSMRST# C16C
RSMRST#
<30> SUS_PWR_ACK M1C
SUS_PWR_ACK
PBTN_OUT# P5C
PBTN_OUT#
<30> EC_ACIN CH751H-40PT_SOD323-2 P7C
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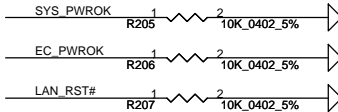
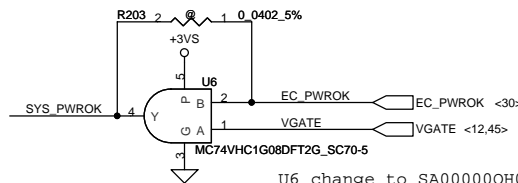
SYS_RESET#
SYS_PWROK
PWROK
MEPWROK
LAN_RST#
DRAMPWROK
RSMRST#
SUS_PWR_DN_ACK / GPIO30
PWRBTN#
ACPRESENT / GPIO31
BATLOW# / GPIO72
RI#
IBEXPEAK-M_FCBGA107

System Power Management

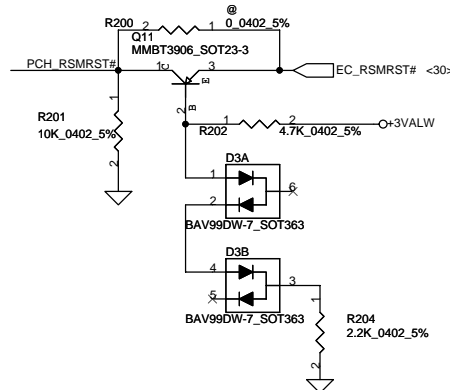
WAKE# J12 PCH_PCIE_WAKE# PCH_PCIE_WAKE# <26,27>
CLKRUN# / GPIO32 Y1 PM_CLKRUN# PM_CLKRUN# <30>
SUS_STAT# / GPIO61 P8 PCH_GPIO61 @ PAD T7
SUSCLK / GPIO62 F3 PCH_SUSCLK <30>
SLP_S5# / GPIO63 E4 PM_SLP_S5# <30>
SLP_S4# H7 PM_SLP_S4# <30>
SLP_S3# E12 PM_SLP_S3# <30>
SLP_M# K8 PM_SLP_M# @ PAD T9
TP23 N2 PM_SLP_DSW# @ PAD T10
PMSYNCH BJ10 H_PM_SYNC <5>
SLP_LAN# / GPIO29 F6 PM_SLP_LAN#

32.768KHZ ouput for remove EC crystal
20091103

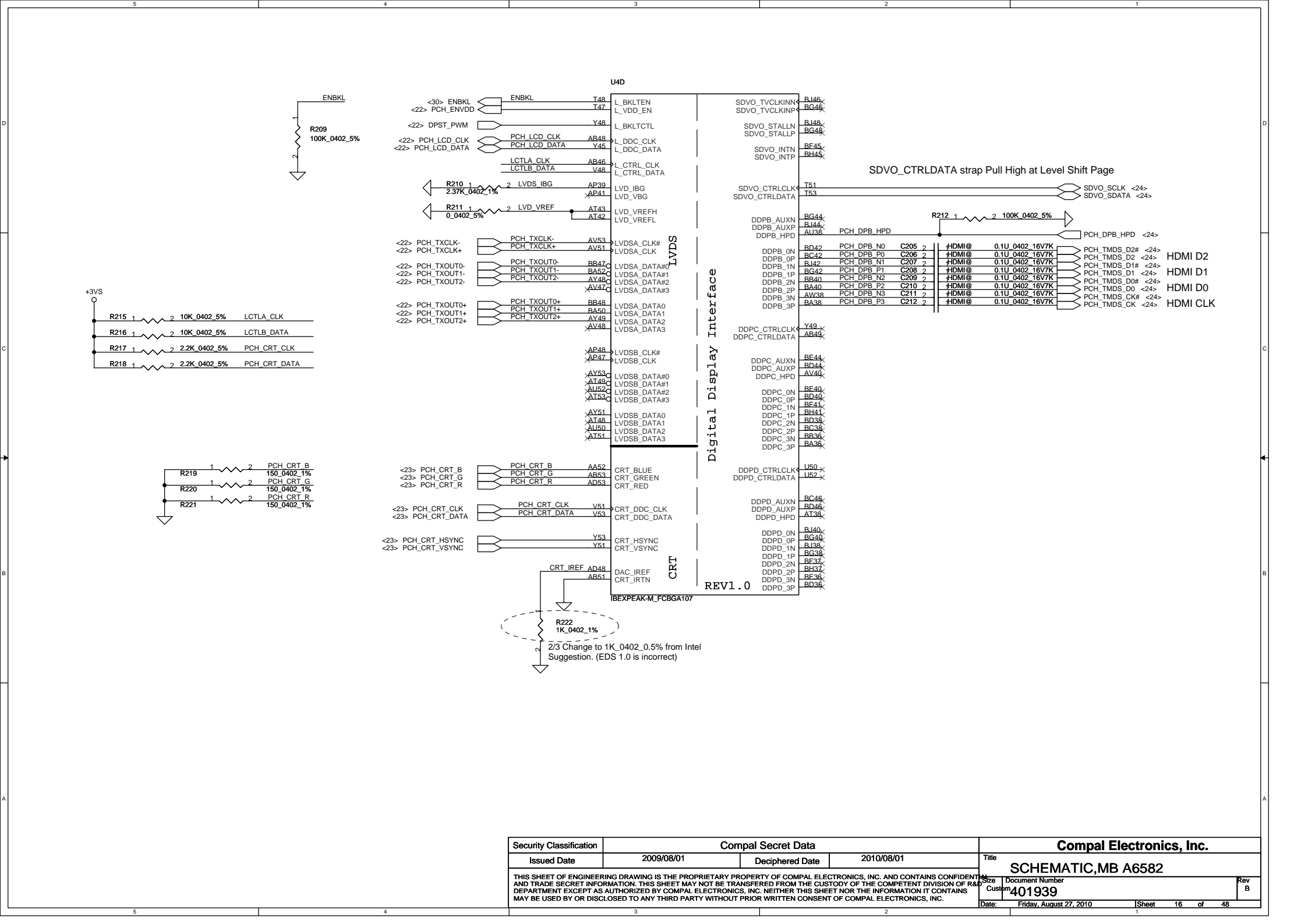
10/2
R199 Intel suggestion change to 10K

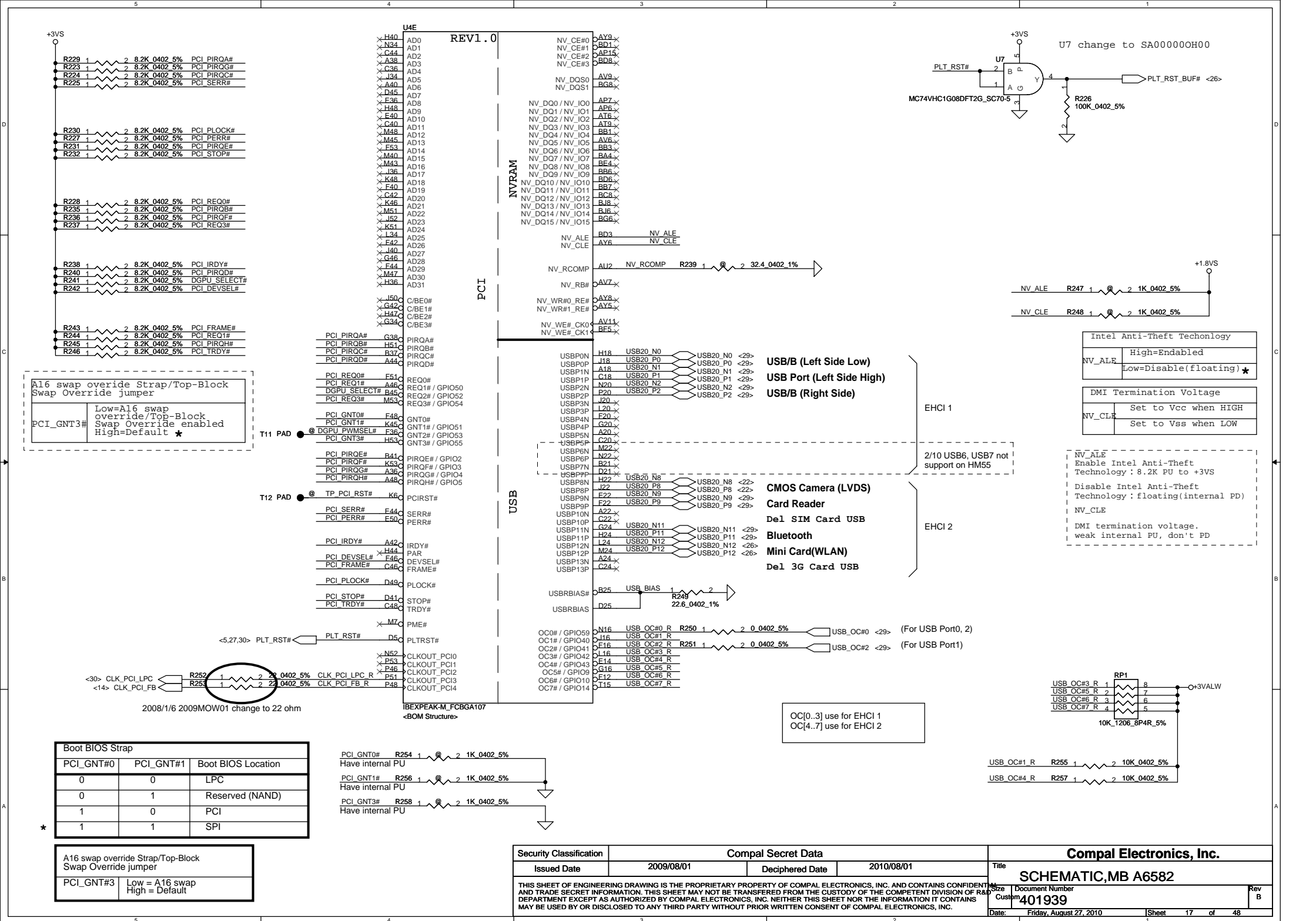


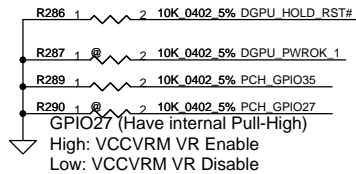
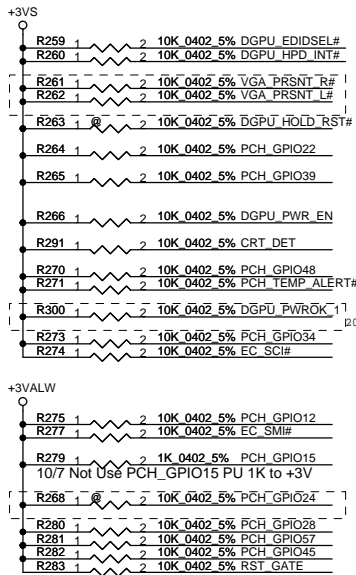
No used Integrated LAN,
connecting LAN_RST# to GND



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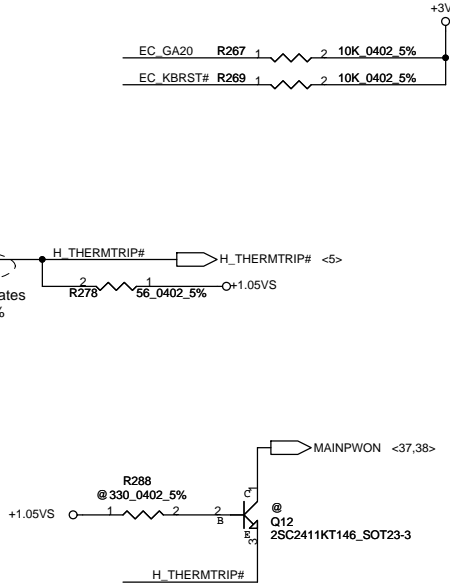
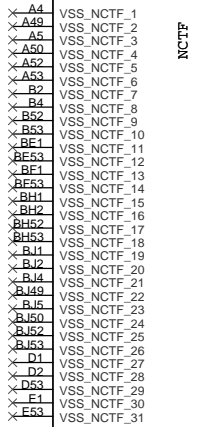
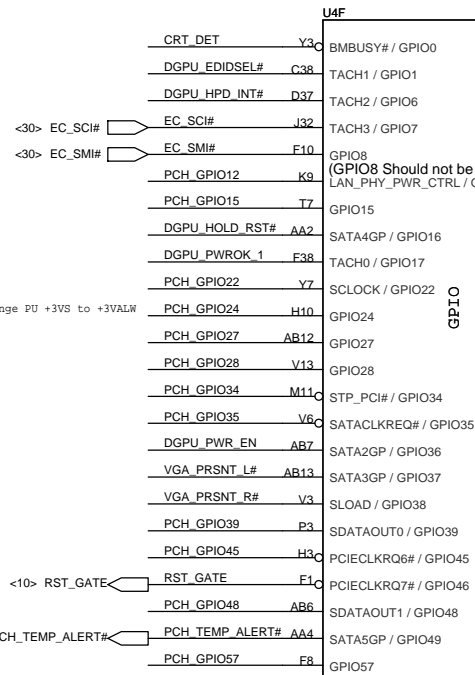
	GPIO19	GPIO37
	PCH_GPIO19	VGA_PRSENT_L#
dGPU	0	0
iGPU	0	1
SG	1	0

GPIO27
On-Die PLL Voltage Regulator
This signal has a weak internal pull up

* H: On-Die voltage regulator enable
L: On-Die PLL Voltage Regulator disable

GPIO8
This signal has a weak internal pull up
can't Pull low

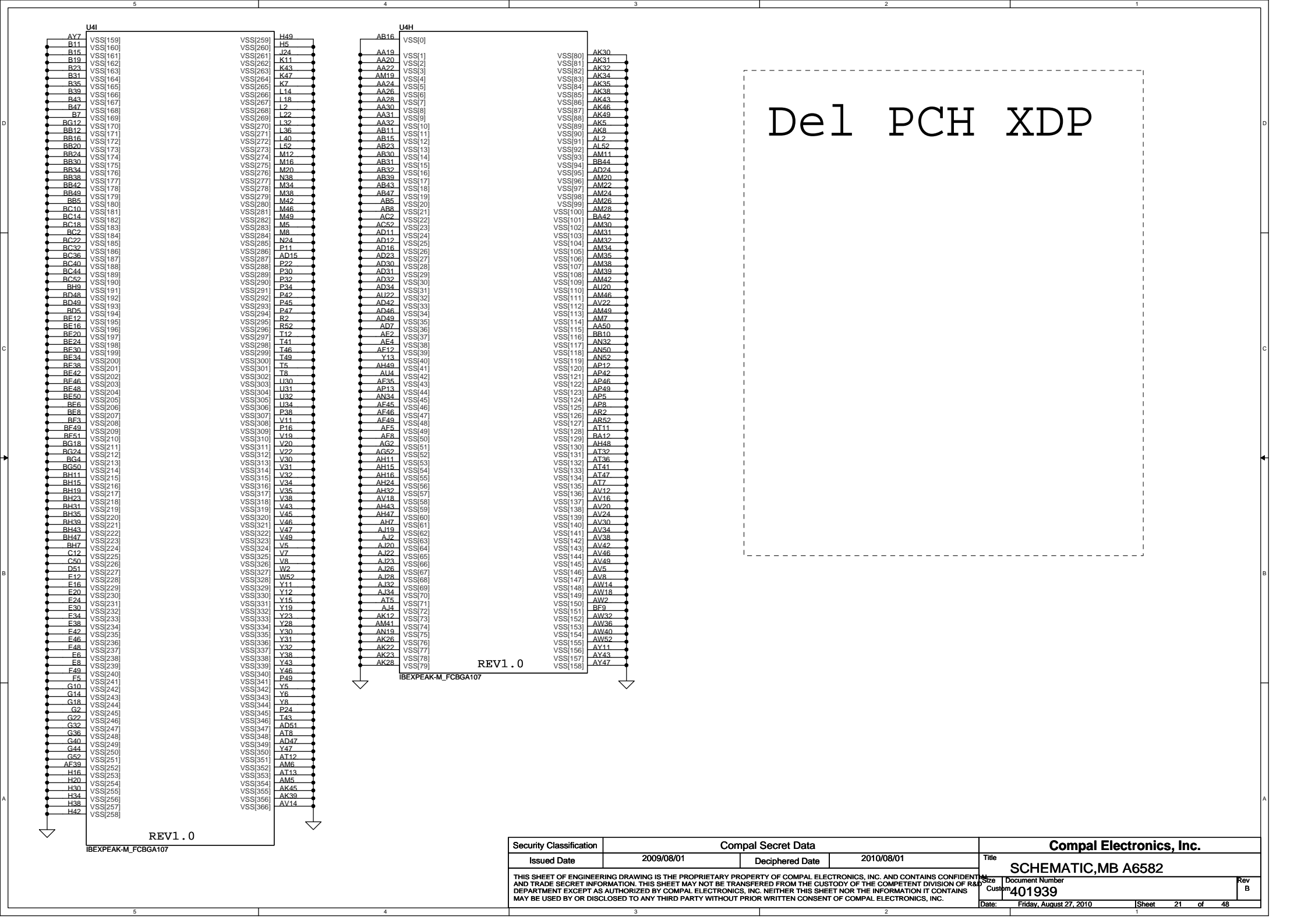
GPIO15
L: Intel ME Crypto Transport Layer Security(TLS) chiper suite with no confidentiality *
H: Intel ME Crypto Transport Layer Security(TLS) chiper suite with confidentiality
It have weak internal PU 20K



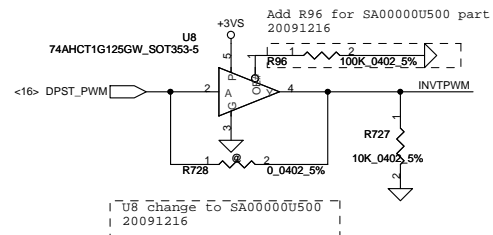
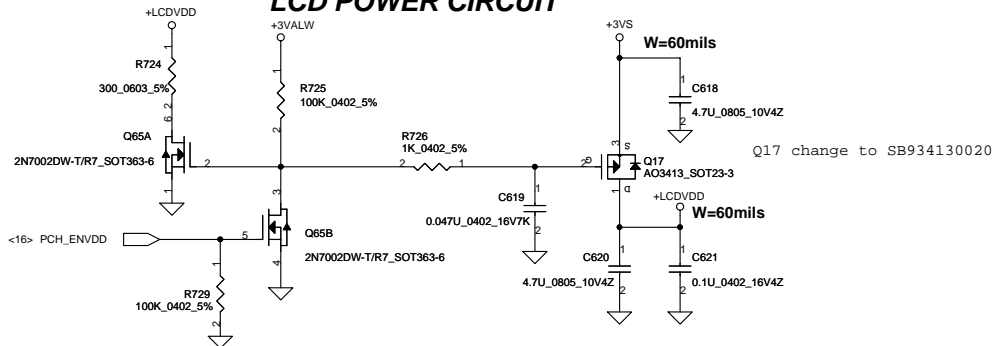
IBEXPEAK-M_FCBGA107

REV1.0

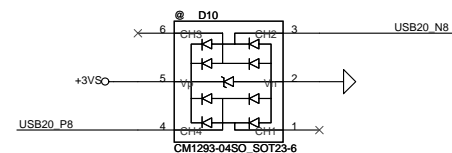
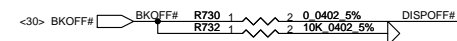
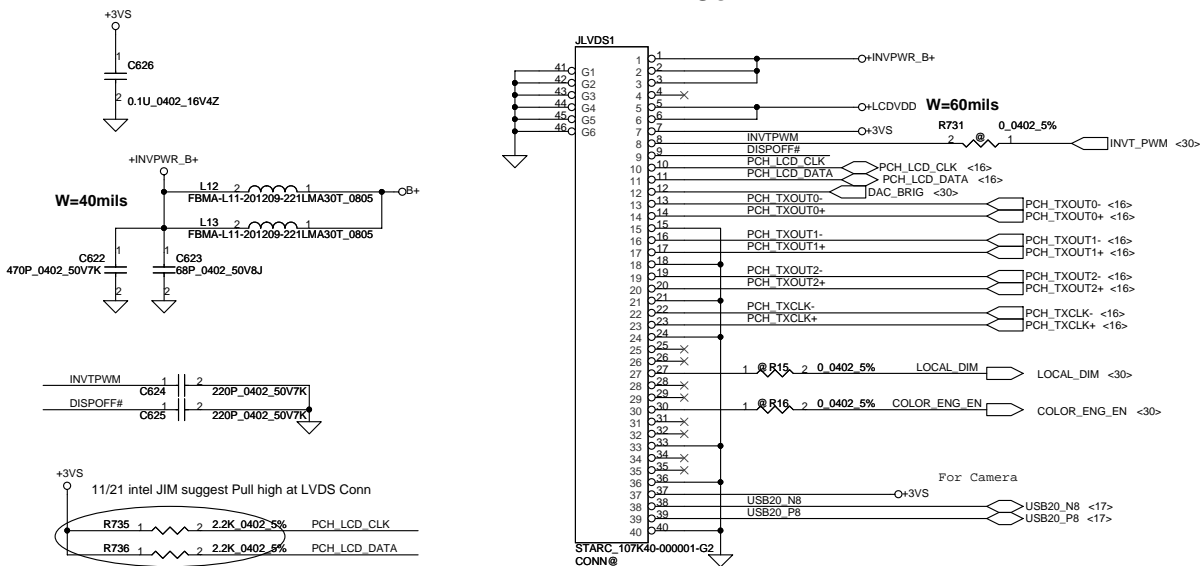
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LCD POWER CIRCUIT

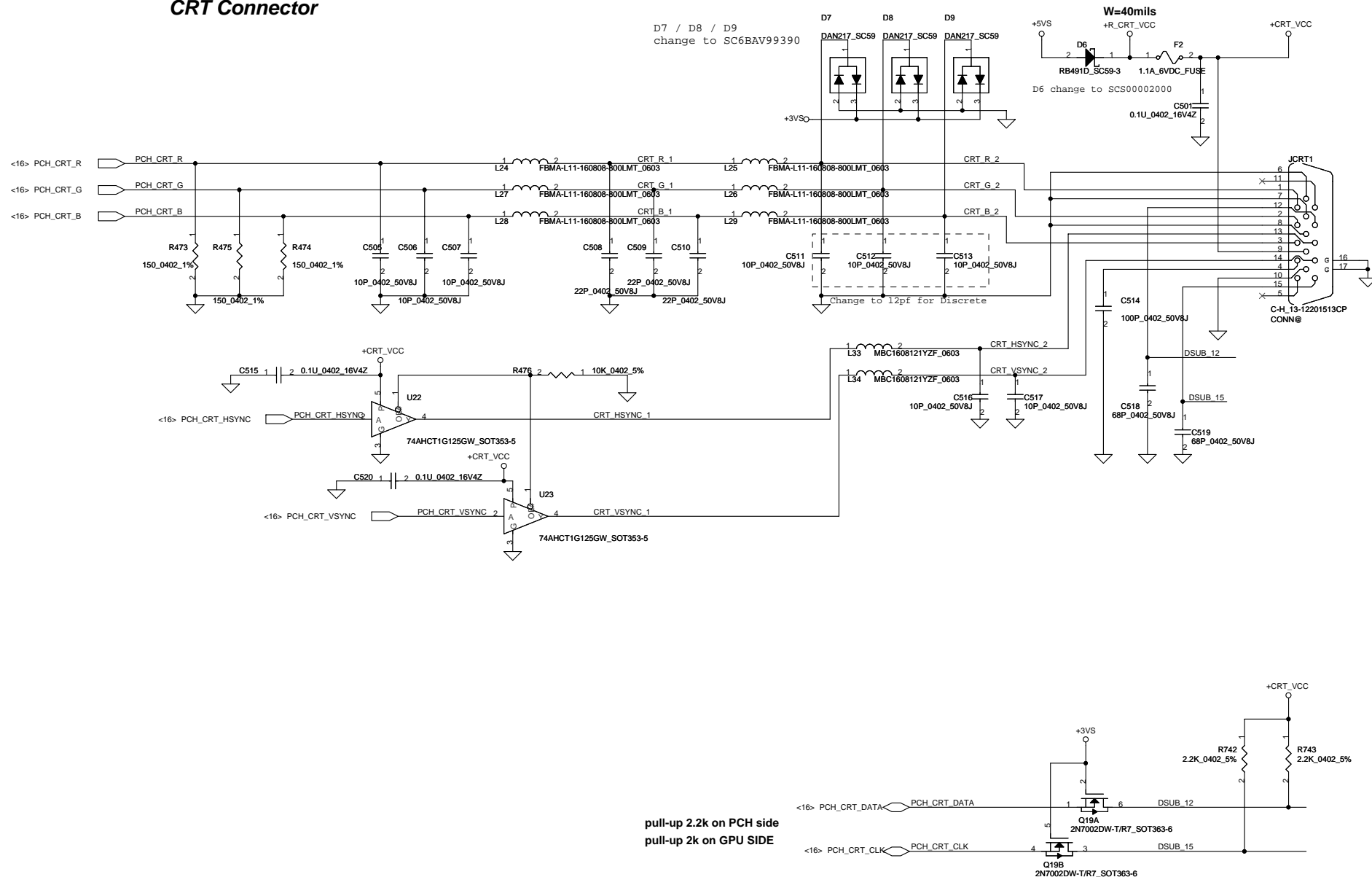


LED PANEL Conn.



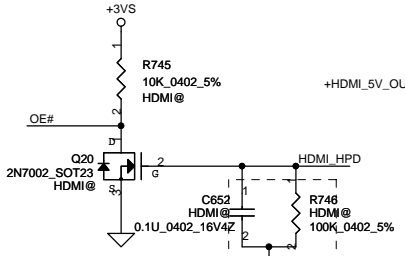
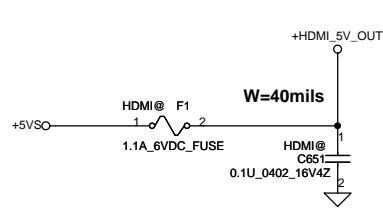
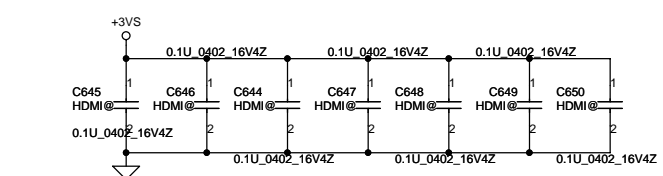
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CRT Connector

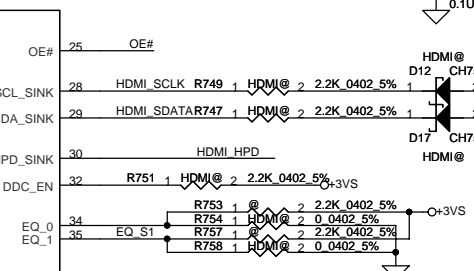
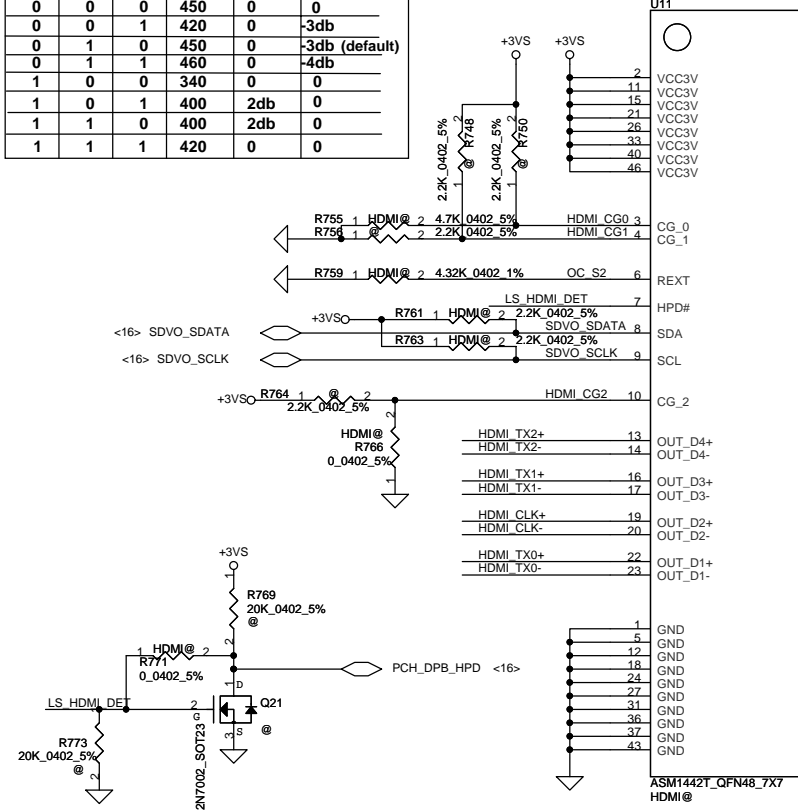
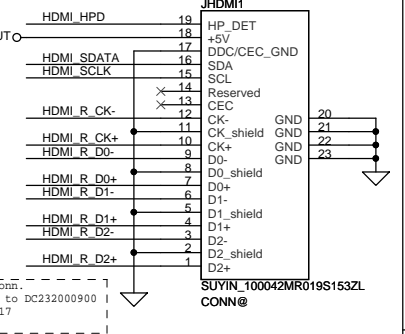


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								Document Number		Rev B	
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								Date		Friday, August 27, 2010	
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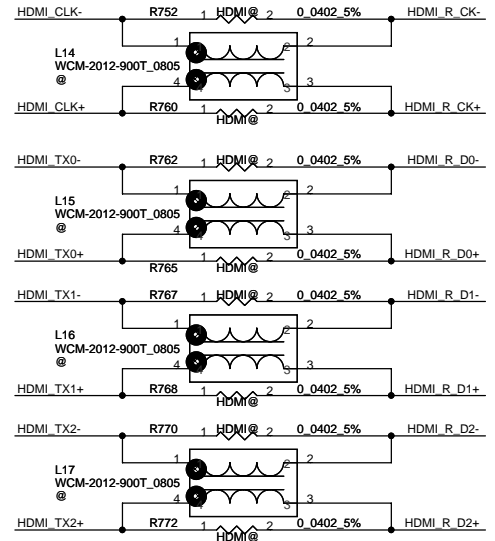
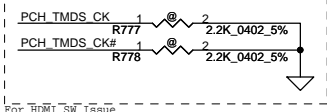
CG0	CG1	CG2	Swing	Pre-amp	Slew-rate
0	0	0	450	0	0
0	0	1	420	0	-3db
0	1	0	450	0	-3db (default)
0	1	1	460	0	-4db
1	0	0	340	0	0
1	0	1	400	2db	0
1	1	0	400	2db	0
1	1	1	420	0	0



NAL00 HDMI connector

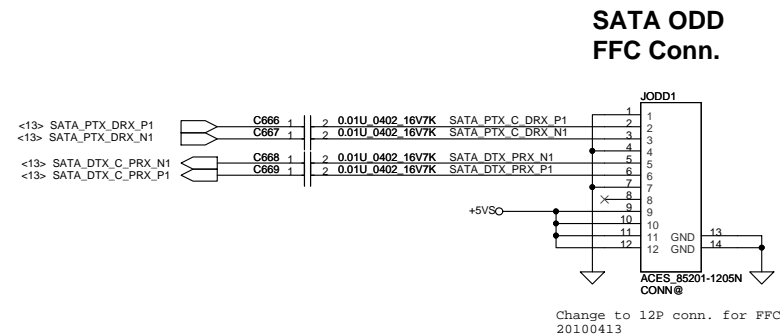
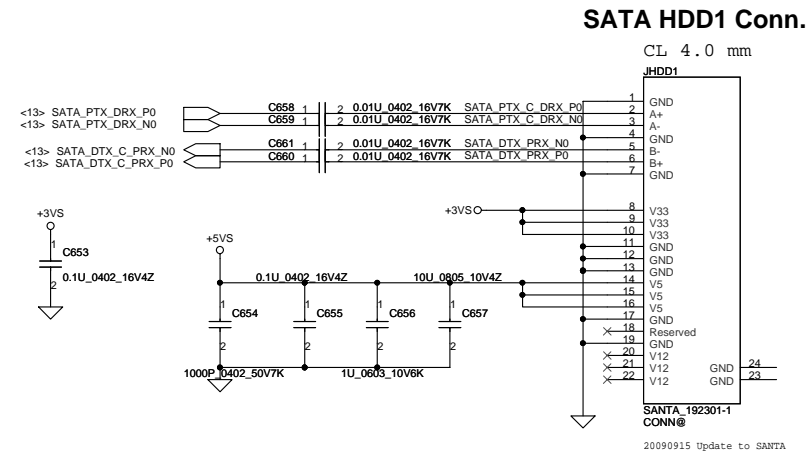
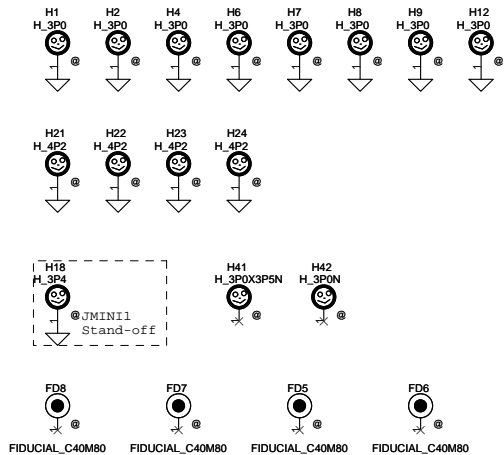


EQ0	EQ1	Equalization
0	0	12dB
0	1	9dB
1	0	6dB
1	1	3dB (default)



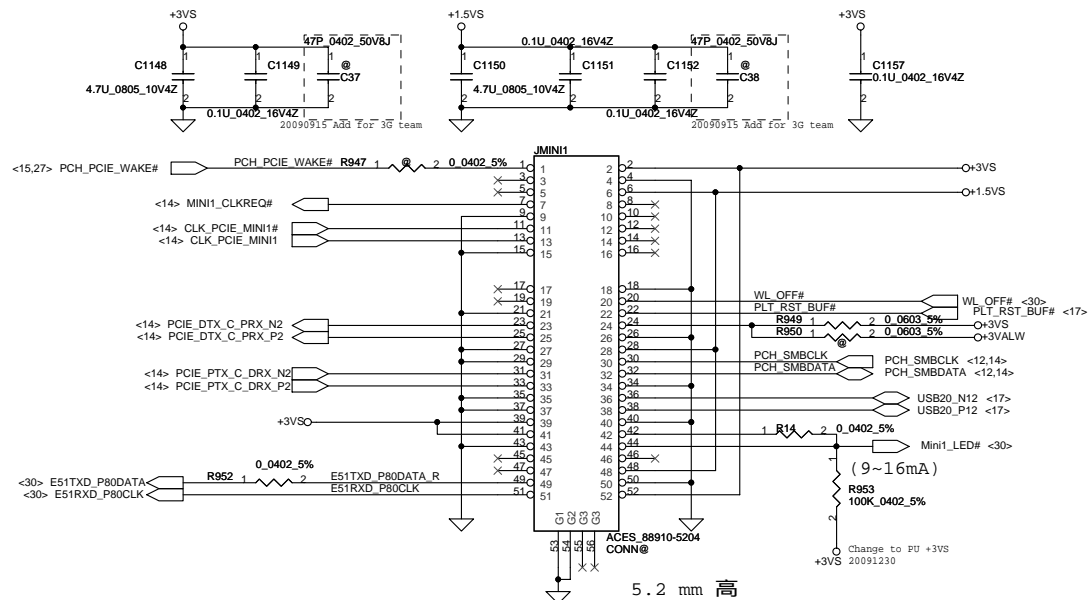
Change to TI P/N: SA00003DS00
20100608

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For Wireless LAN

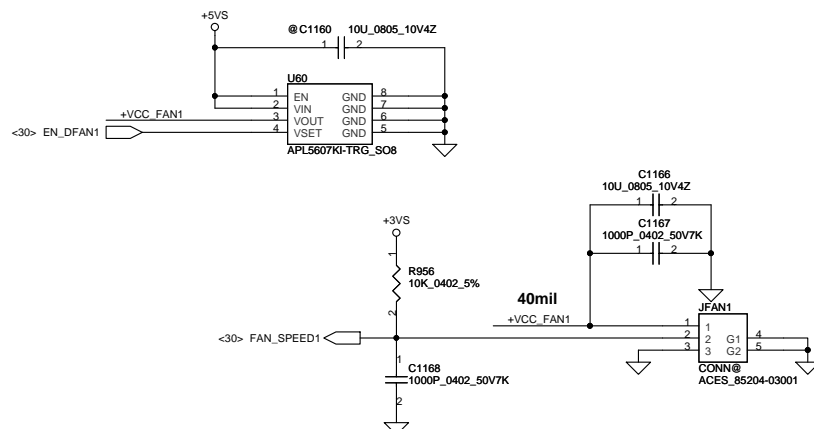


Mini Card Power Rating

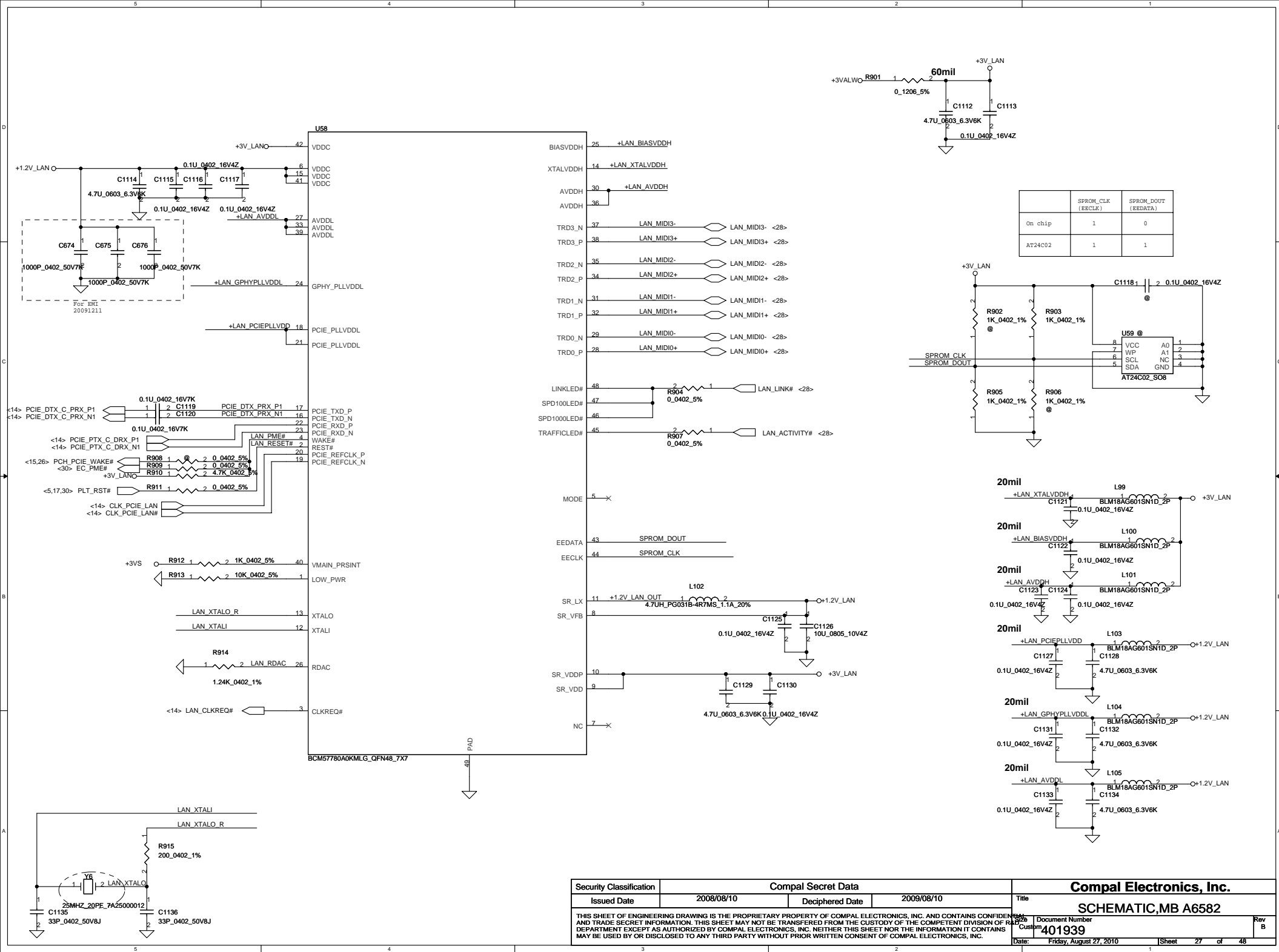
Power	Primary Power (mA)		Auxiliary Power (mA)
	Peak	Normal	Normal
+3VS	1000	750	
+3V	330	250	250 (wake enable)
+1.5VS	500	375	5 (Not wake enable)

Del 3G / GPS Module Connect

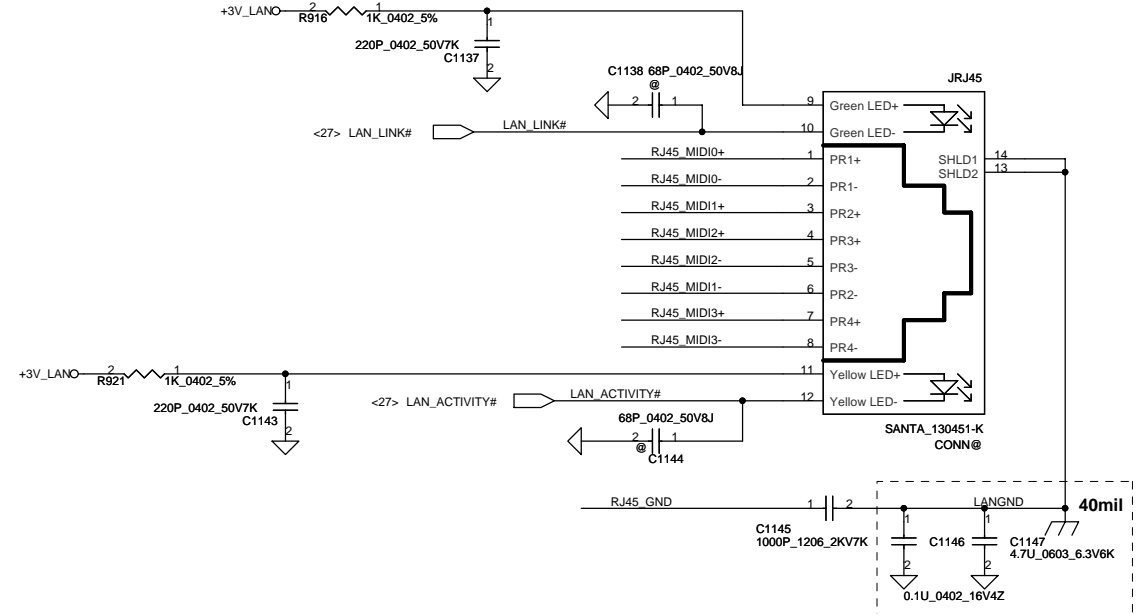
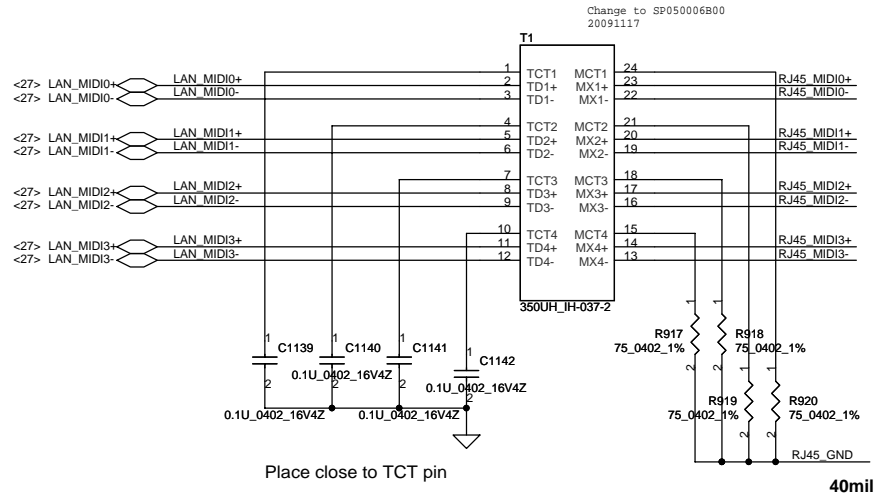
FAN1 Conn



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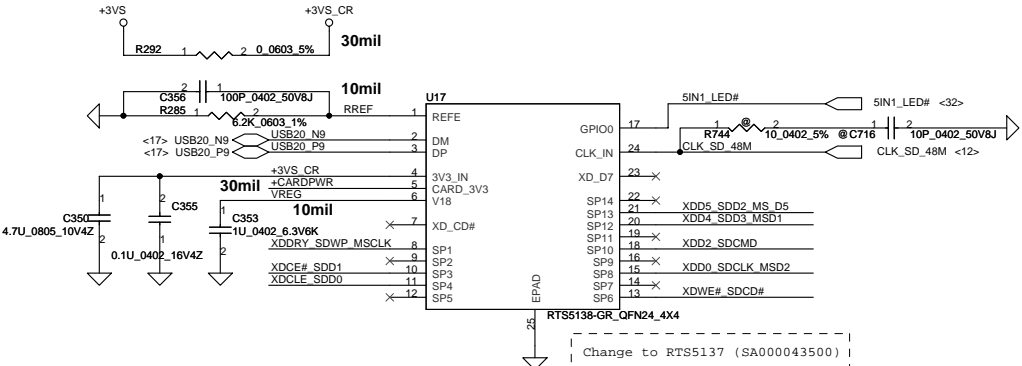


LAN Connector

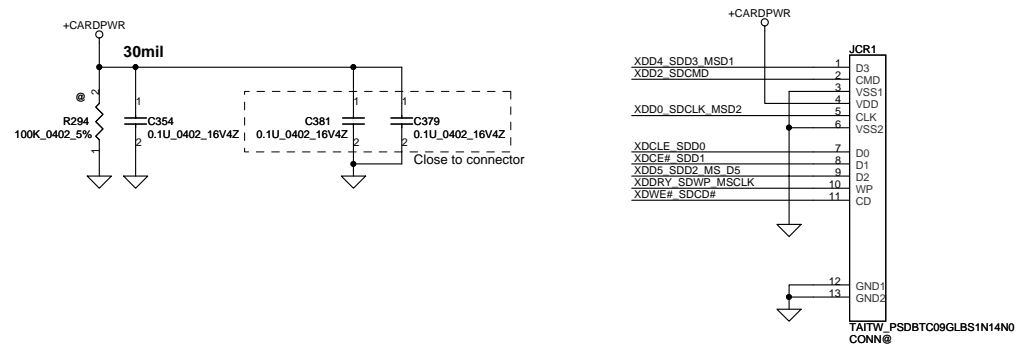


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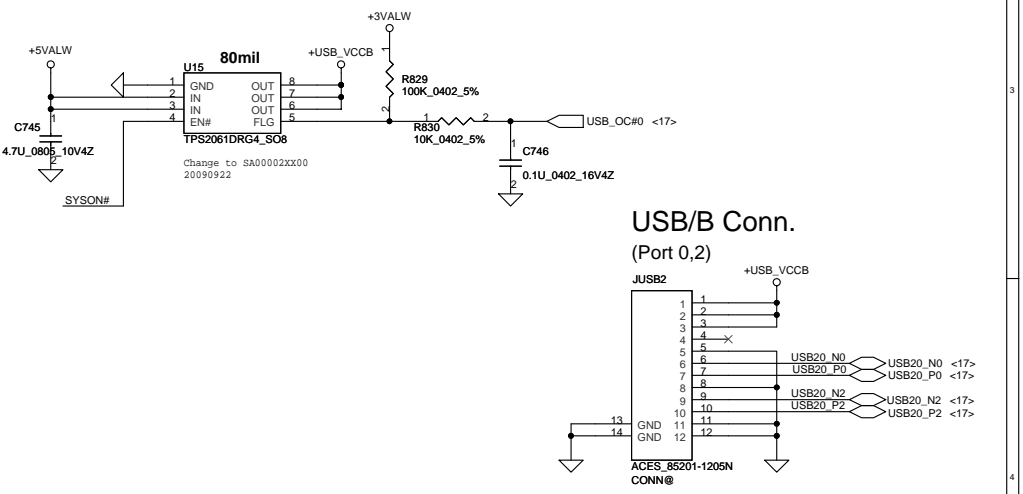
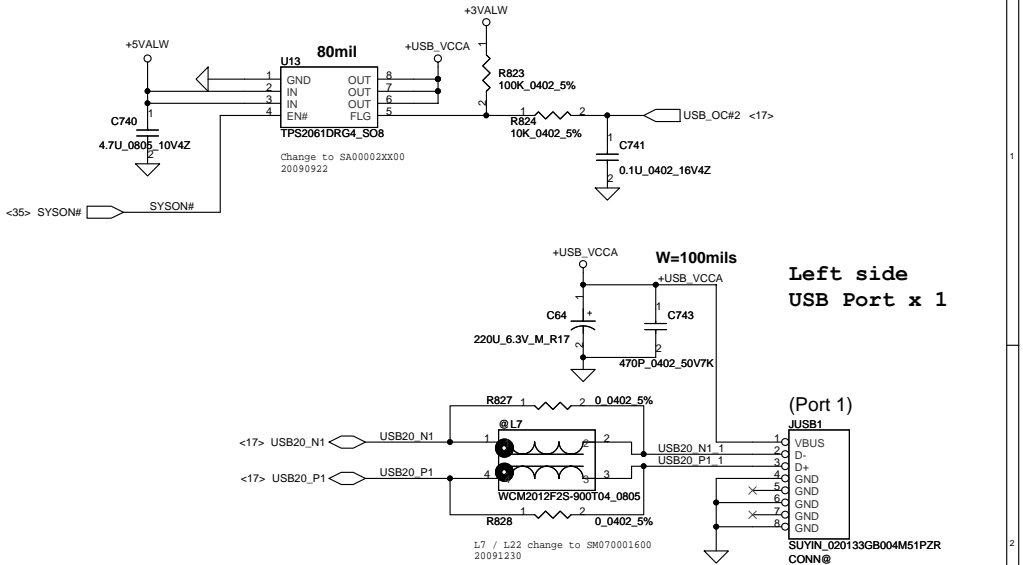
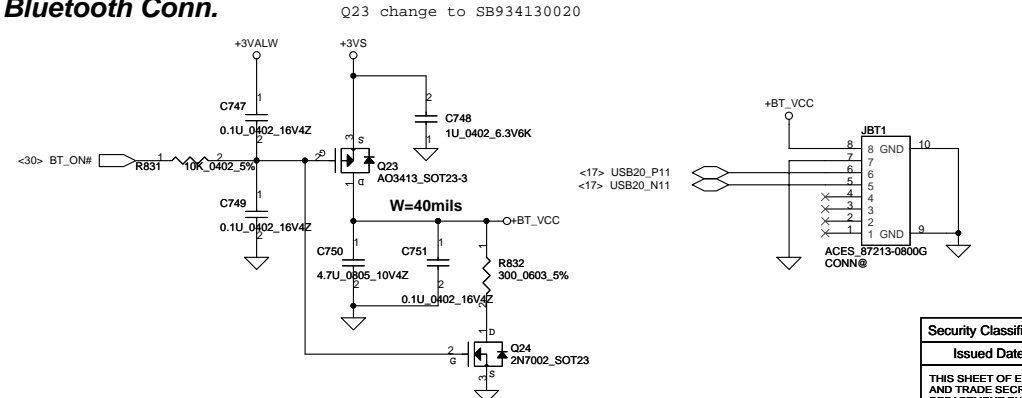
Card Reader RTS5138 / RTS5137
(only SD+MMC function)



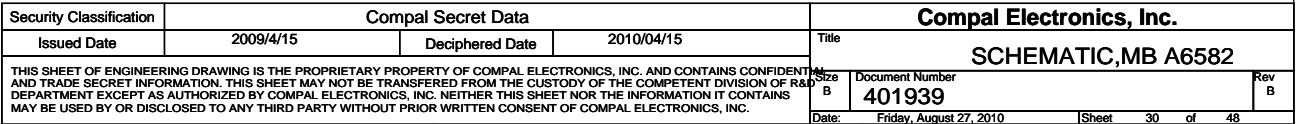
Card Reader Connector

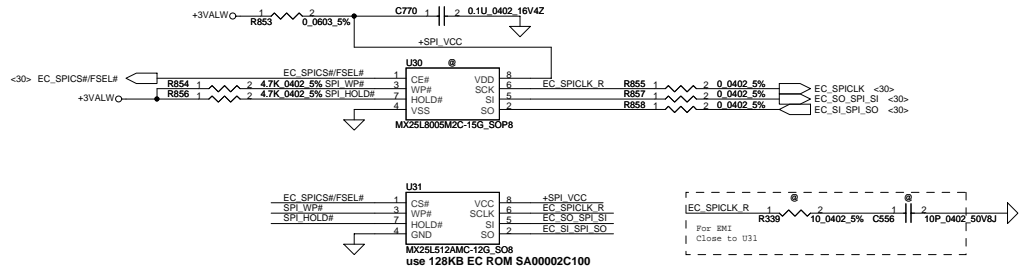
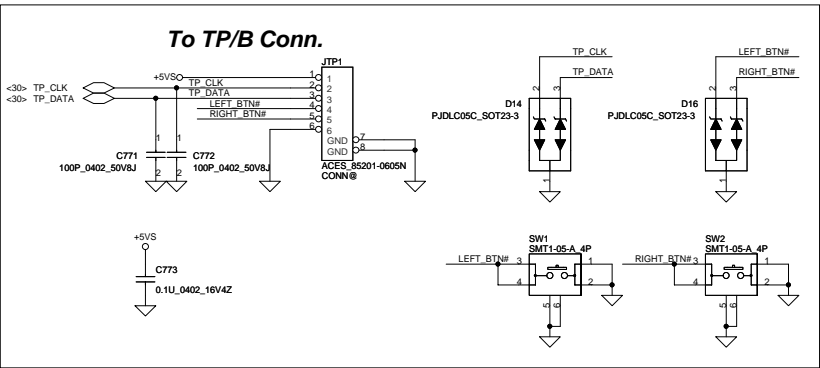
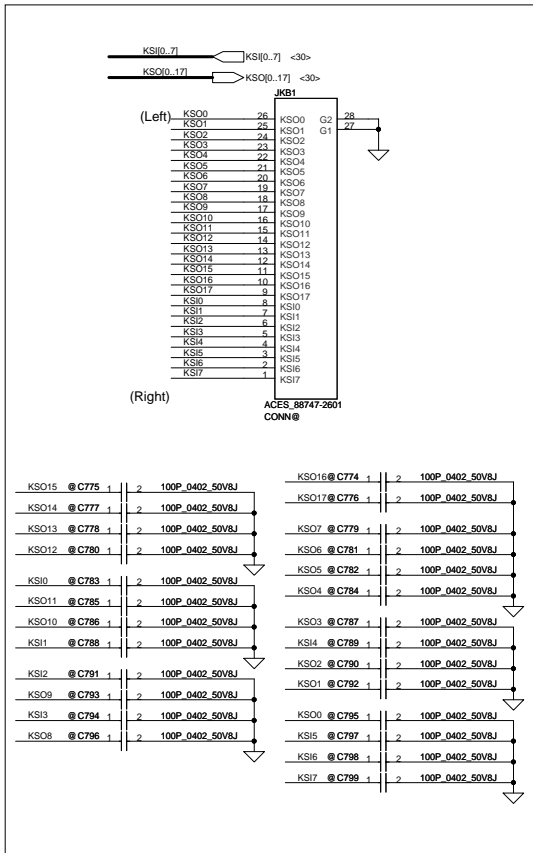


Bluetooth Conn.



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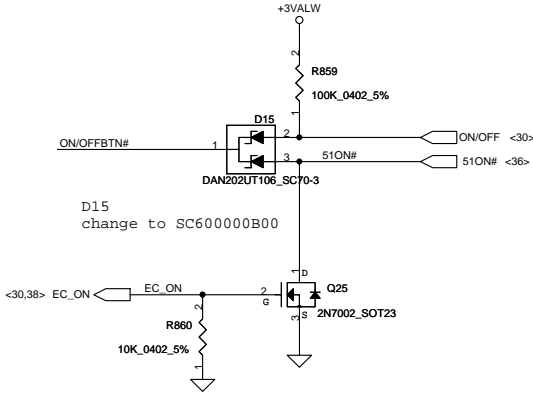


ENE suggestion SPI Frequency over 66MHz
 SST: 50MHz
 MXIC: 70MHz
 ST: 40MHz

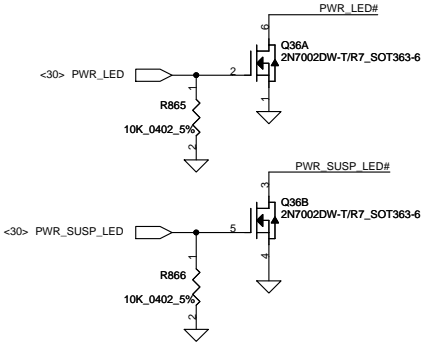
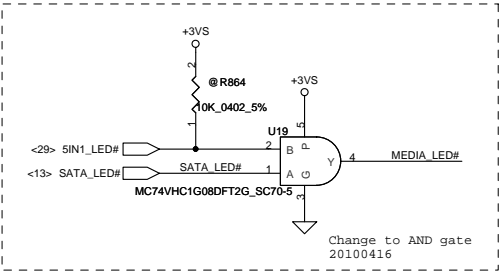
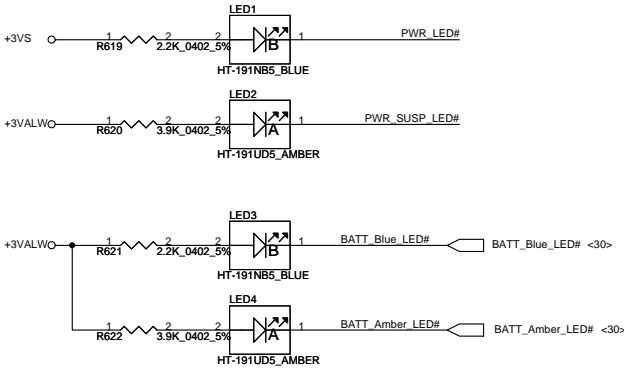
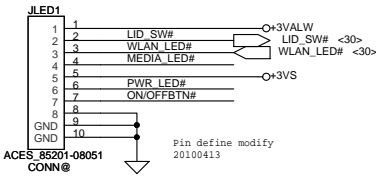
To BTN/B Conn.

KSO0	KSO3
KSI1	WL_BTN#
KSI2	T/P lock_BTN#
KSI3	Back up_BTN#
KSI4	BT_BTN#
KSI5	Power save_BTN#

Power Button

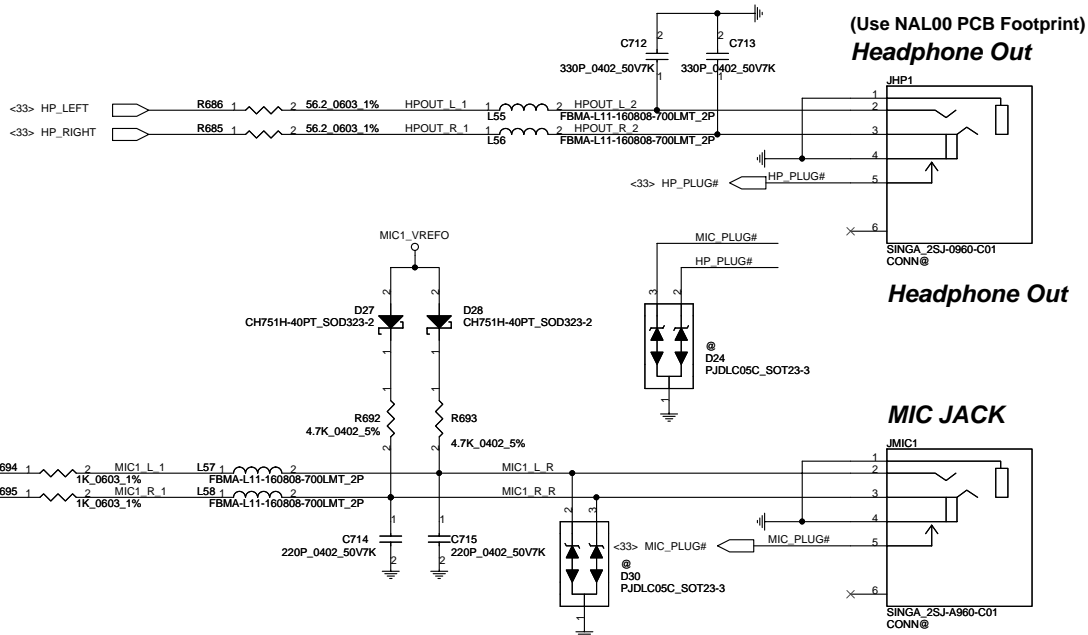
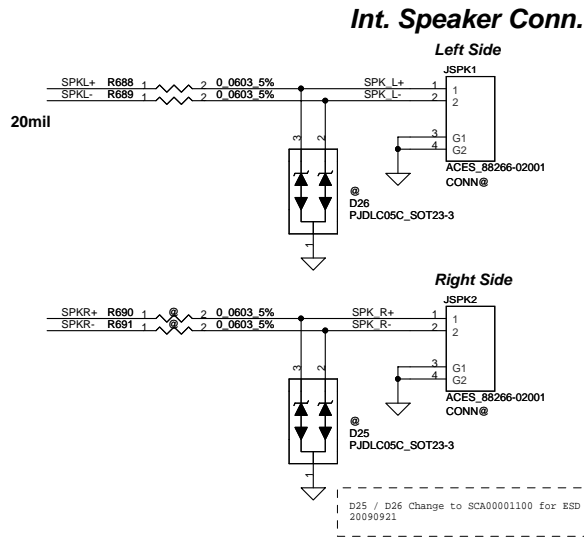
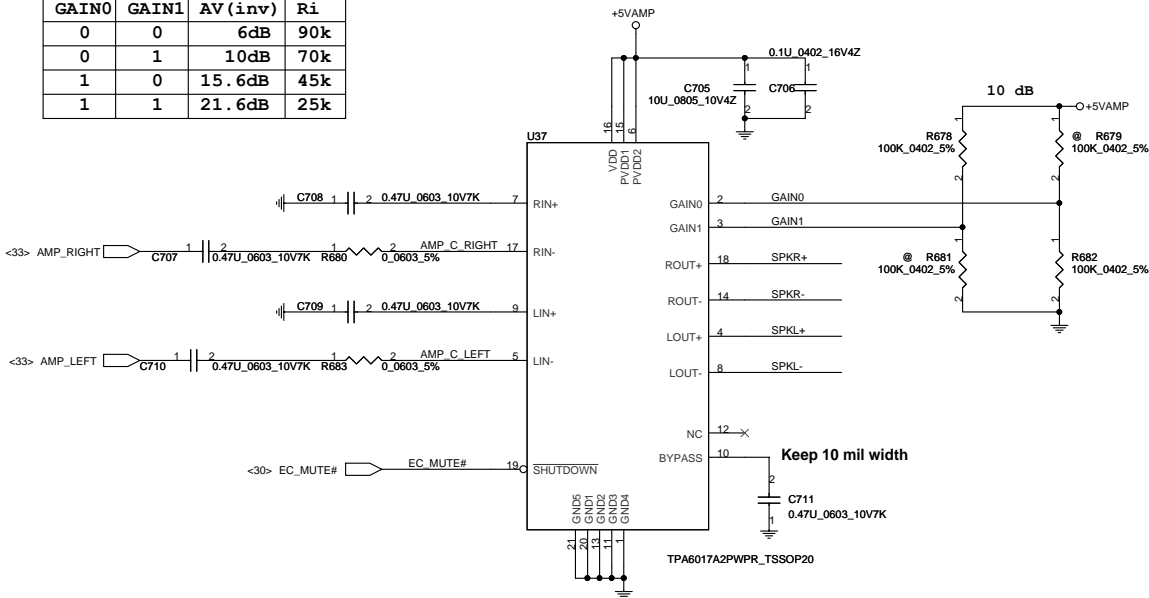


LED/B LEFT



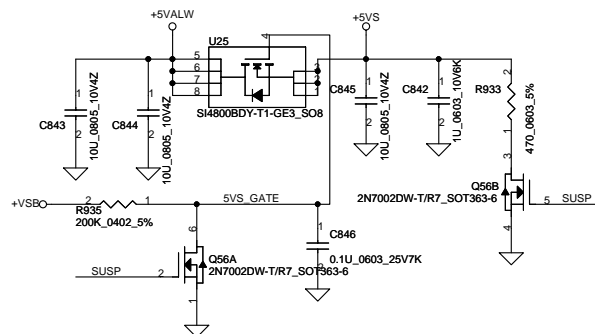
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										SCHEMATIC, MB A6582	
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GAIN0	GAIN1	AV (inv)	Ri
0	0	6dB	90k
0	1	10dB	70k
1	0	15.6dB	45k
1	1	21.6dB	25k

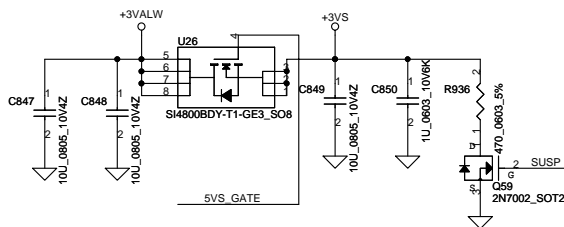


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										Customer		Document Number		401939	
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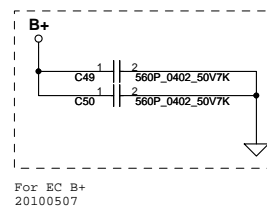
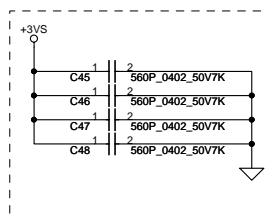
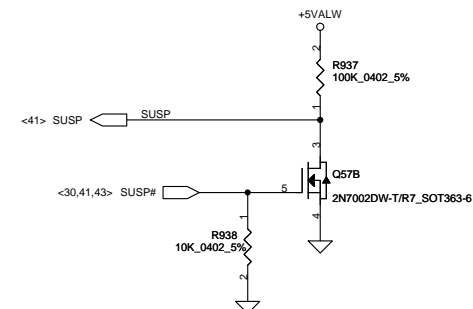
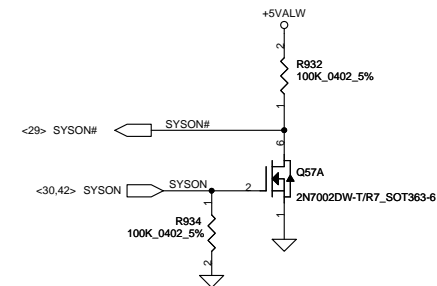
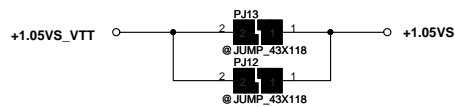
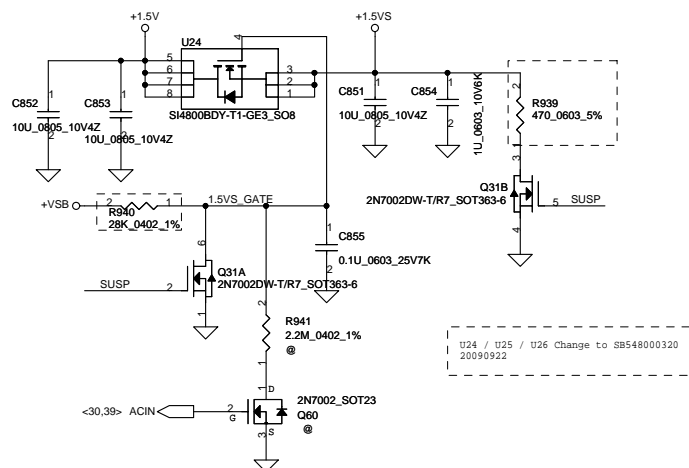
+5VALW TO +5VS



+3VALW TO +3VS

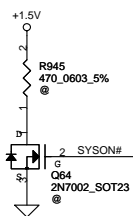
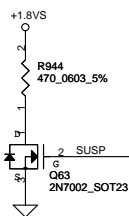
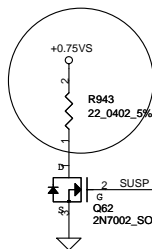
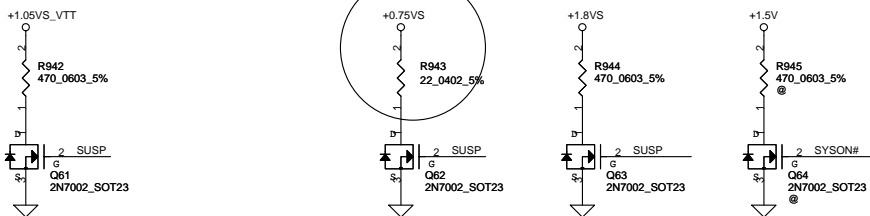


+1.5V to +1.5VS

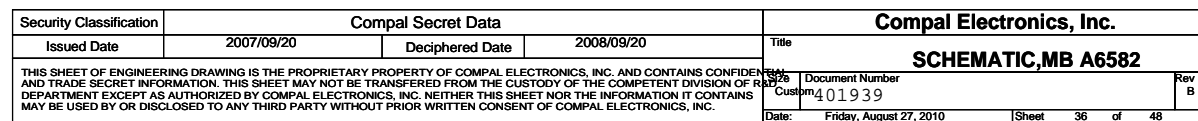
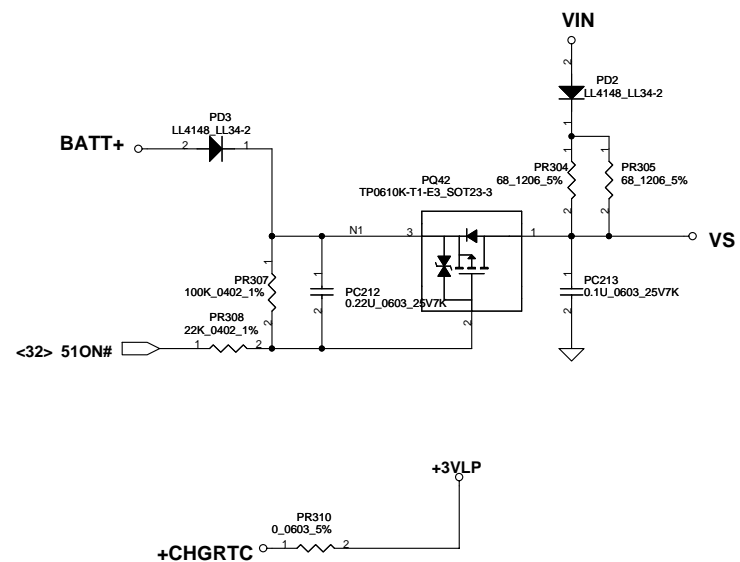


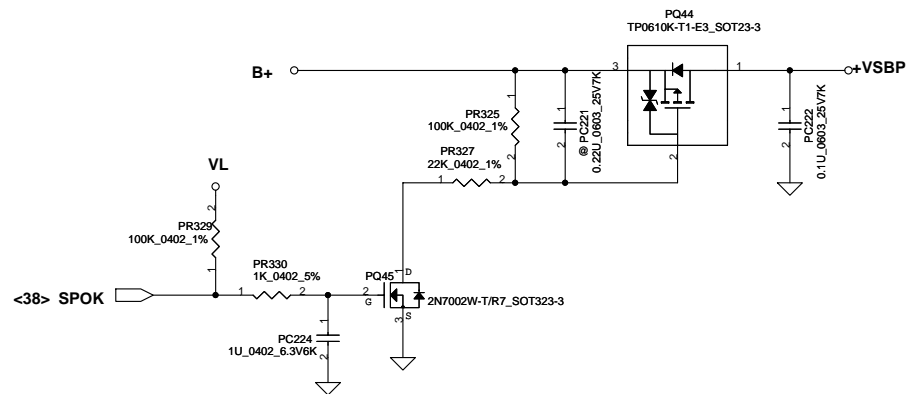
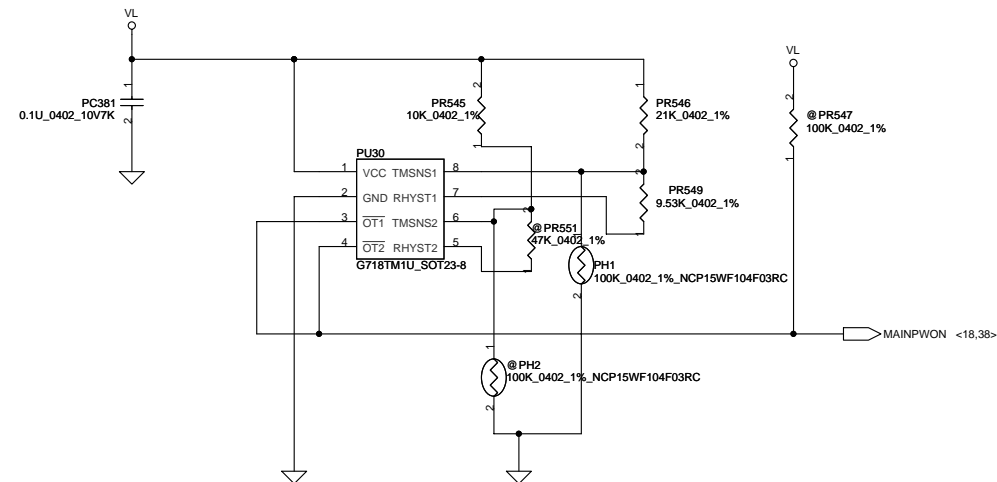
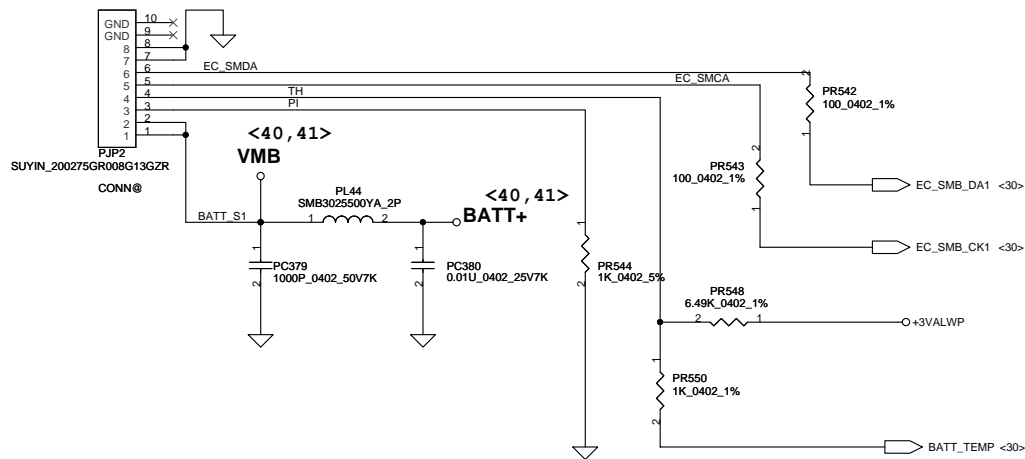
For LAM Common mode noise
2009111102330

For EC B+
20100507



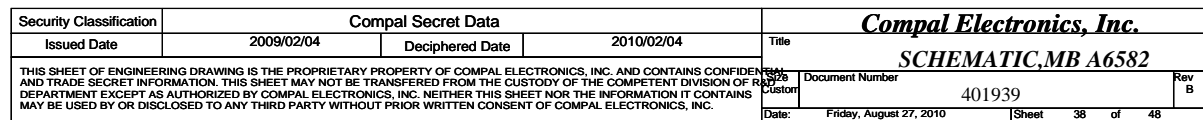
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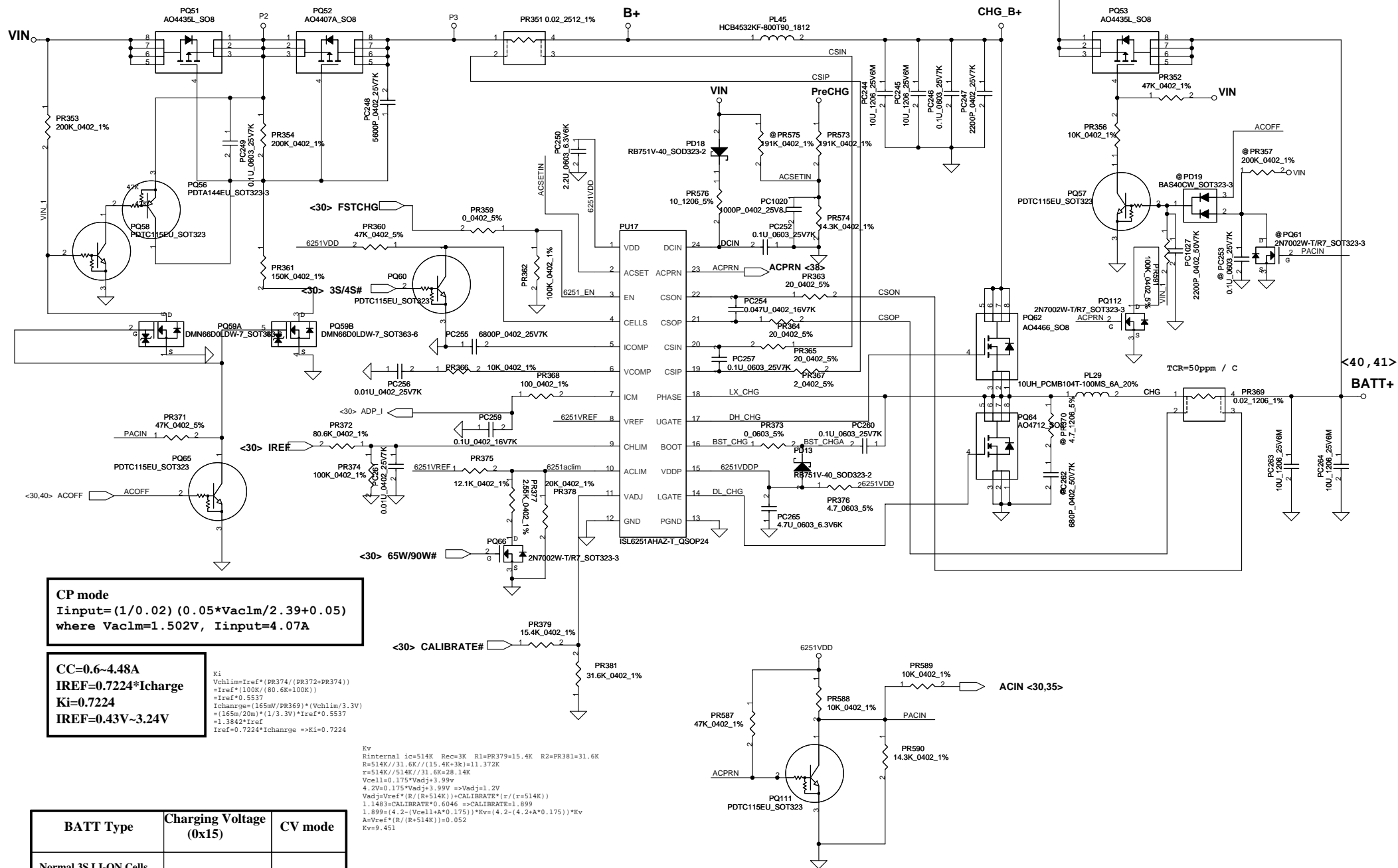
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Use TPS51125 IC can remove RTC refernece LDO
Use TPS51427 IC must keep RTC refernece LDO
```



Iada=0~4.74A (90W/19V=4.736A)
Iada=0~3.42A (90W/19V=3.421A)

ADP_I = 19.9*Iadapter*Rsense

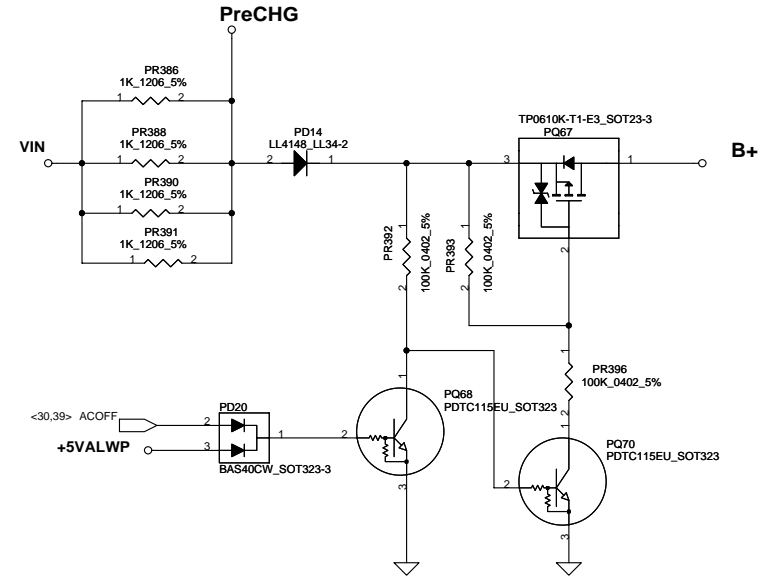
CP = 85*Iada ; CP = 4.07A
CP = 85*Iada ; CP = 2.91A



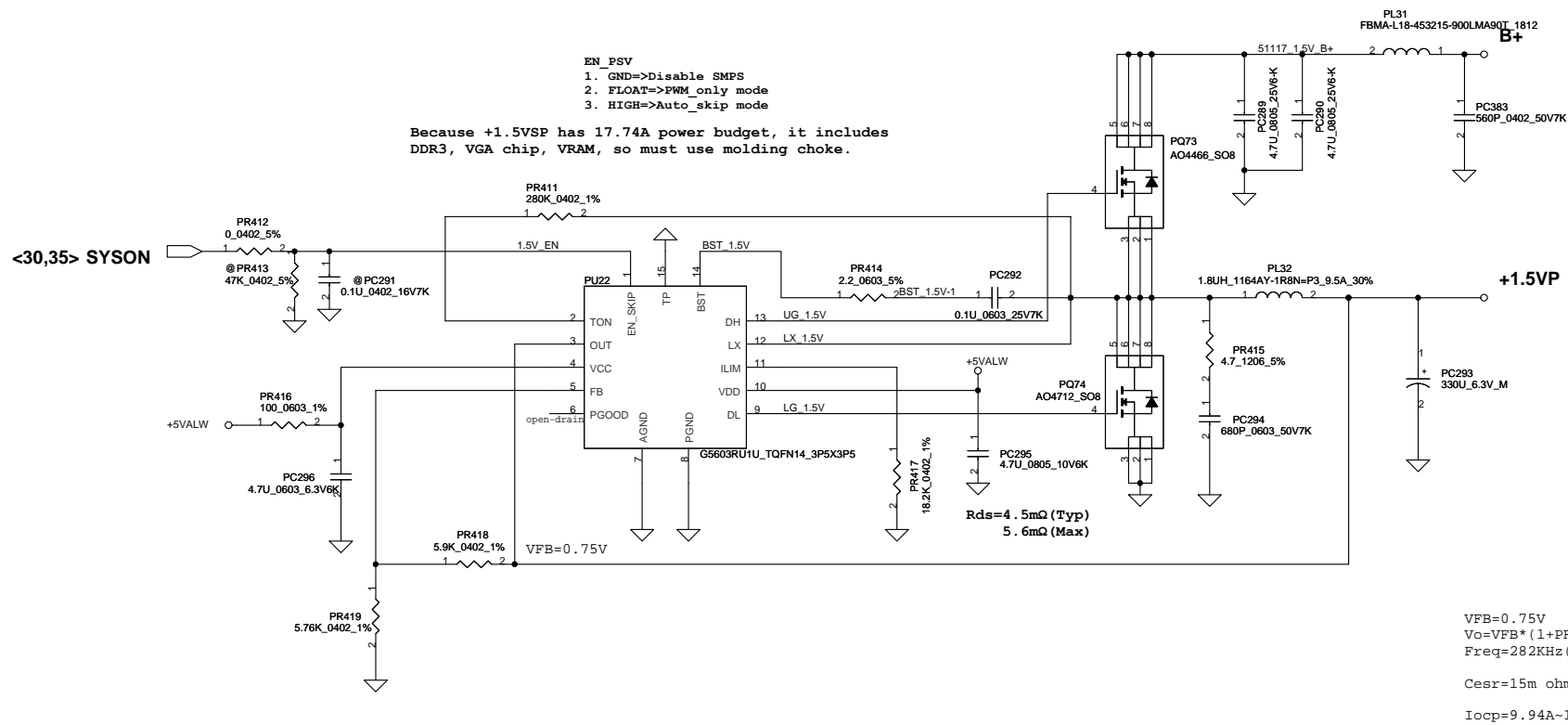
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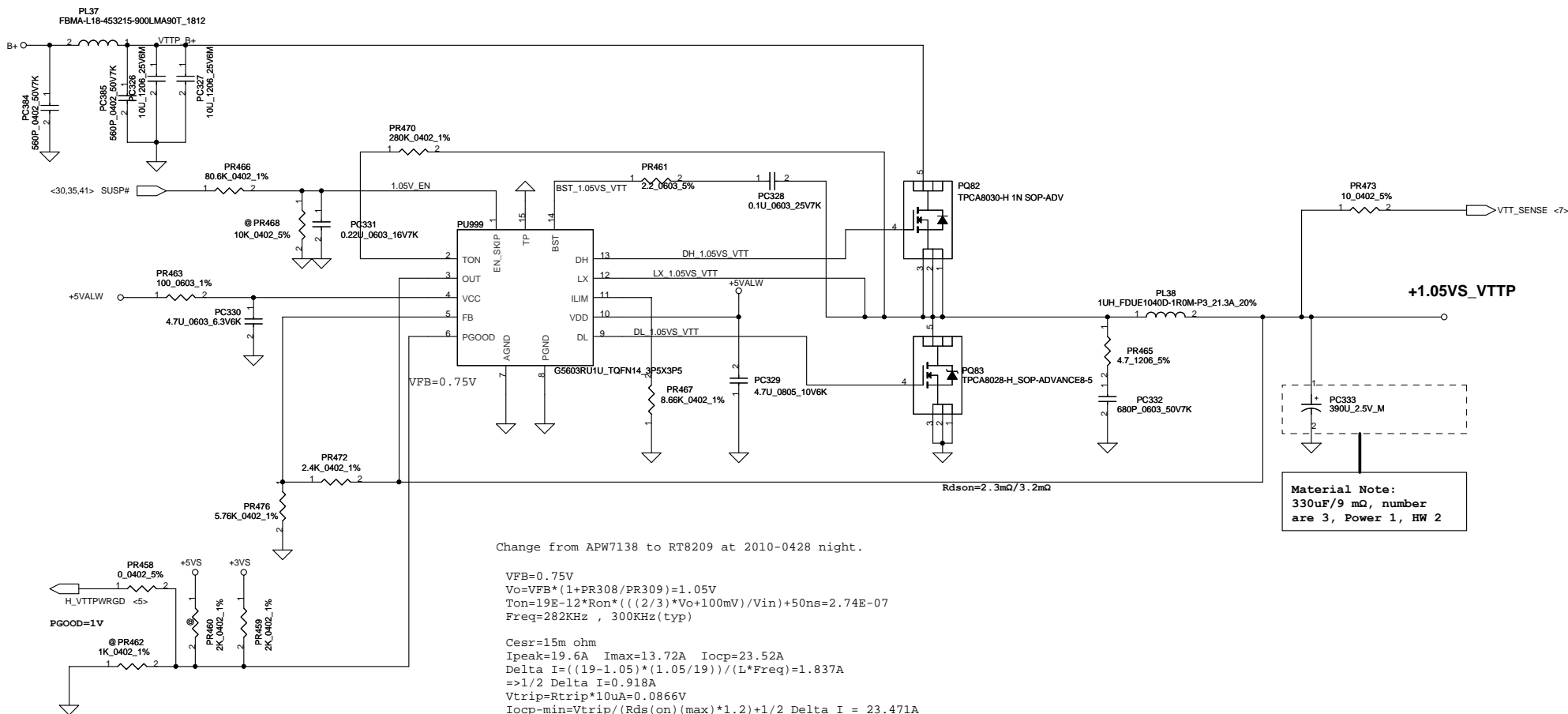
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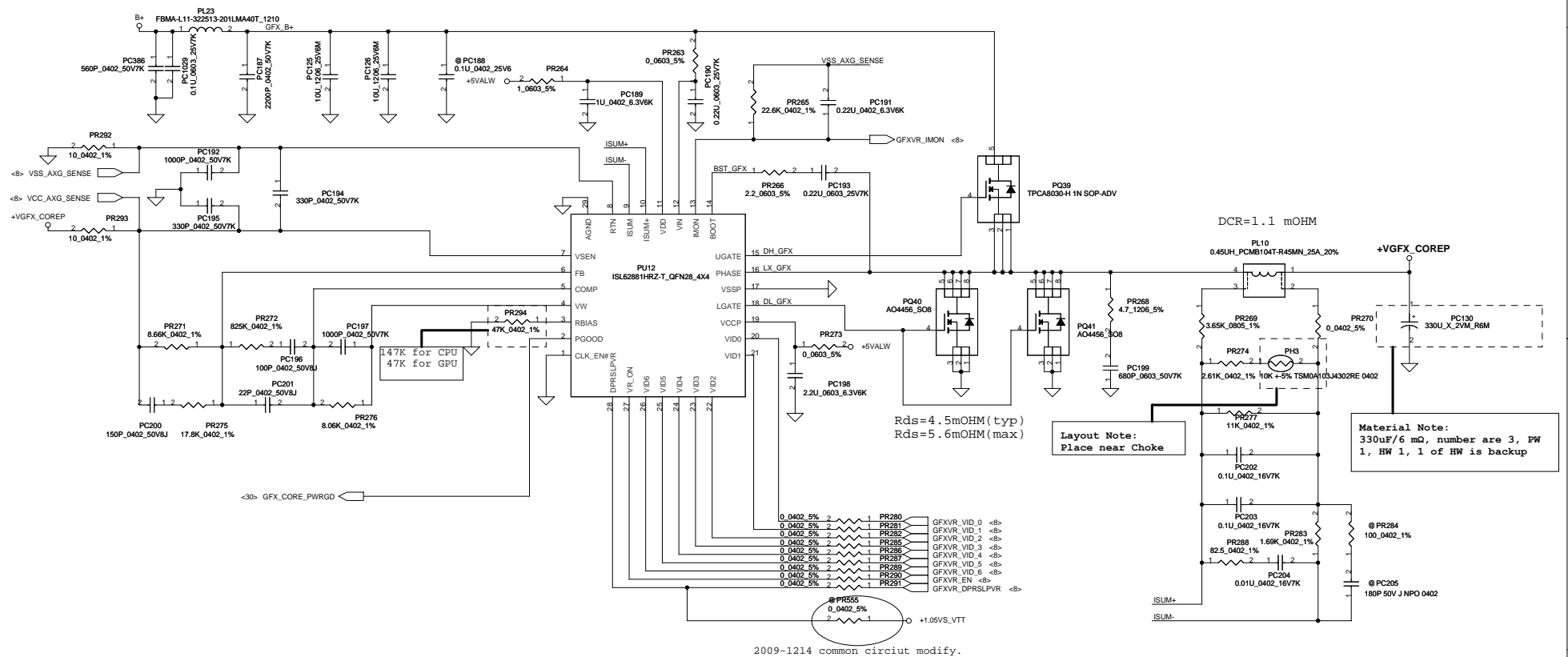


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Intel Auburndale CPU(Integrated Graphics) Ipeak=22A Imax=15A
OCF calculation : Assume DCR=1.1m ohm
G1=Rn/(Rn+Rsum)=0.617
where Rn=FR277 // (R274+PH3)=5.875k ohm
Rsum=FR269=3.65k ohm
LL=2*Rdroop*G1*DCR/Ri= 6.96m V/A
where Rdroop=FR271=8.66k ohm, Ri=PR283=1.69k ohm
Iocp=OCF Threshold*Rdroop/LL=24.89A

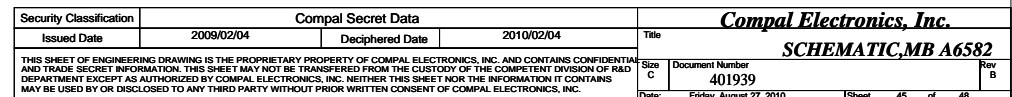
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2009-1214 common circiut modify.

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	HFM_VID	HFM_Icc	LL	Icc_TCD	Icc_Dy
Auburndale 45W	1.075	50	1.9m	37	35
Auburndale 35W	0.975	38	1.9m	29	27
Clarksfield SV	0.95	51	1.9m	38	39
Clarksfield XE	0.95	65	TBD	48	TBD



Item	Fixed Issue	Reason for change	Rev.	PG#	Modify List	Date	Phase
1	Pre-Charge circiut update.	Pre-Charge circiut disable in EVT, now modify and enable it.	0.1	39	Change PR353 from SD034470280 to SD034200380.	2010/06/08	to PVT
2					Change PR371 from SD034220280 to SD034470280.		
3					Change PR587 from SD034100380 to SD034470280.		
4	Pre-Charge circiut update.	Pre-Charge circiut disable in EVT, now modify and enable it.	0.1	38	Change PR590 from SD034200280 to SD034143280. Change PR591 from SD028470280 to SD028100380. Add PC1027 SE074222K80(S CER CAP 2200P 0402 50V7K) Change PQ111 from SB000006800 to SB301150200 Add PQ102 and PQ103 SB301150200. Add PR592 SD034200380.	2010/06/08	to PVT
5	Pre-Charge circiut update.	Pre-Charge circiut disable in EVT, now modify and enable it.	0.1	40	Change PR386 from SD011000080 to SD011100180. Add PR388, PR390, PR391 SD011100180. Add PD14 SC100001Y80 LL4148_LL34-2	2010/06/08	to PVT
6	EMI fail, add 3/5V snubber and Boost Resister to silve it.	EMI request to solve EMI issue.	0.1	38	Add PR566 and PR567 SD001470B80 S RES 4.7 1206 5%	2010/06/08	to PVT
7					Add PC1012 and PC1013 SE074681K80 S CER CAP 680P 0402 50V7K		
8					Change PR564 and PR565 from SD013000080 to SD013220B80 Add PR415 SD001470B80 4.7 1206 5%.		
9	EMI fail, add 1.5V snubber.	EMI request to solve EMI issue.	0.1	42	Add PC294 SE025681K80 S CER CAP 680P 50V K X7R 0603 Add PR465 SD001470B80 4.7 1206 5%.	2010/06/08	to PVT
10	EMI fail, add 1.05V snubber. and Boost Resister.	EMI request to solve EMI issue.	0.1	43	Add PC332 SE025681K80 S CER CAP 680P 50V K X7R 0603	2010/06/08	to PVT
11	EMI fail, add GFX_CORE snubber.	EMI request to solve EMI issue.	0.1	44	Change PR461 from SD013000080 to SD013220B80 Add PR268 SD001470B80 4.7 1206 5%.	2010/06/08	to PVT
12					Add PC199 SE025681K80 S CER CAP 680P 50V K X7R 0603		
13					Change PR466 from SD034576280 S RES 1/16W 57.6K +-1% 0402 to SD034806280 S RES 80.6K 0402 1%)		
14	HW power sequence modify.	HW request.	0.1	43	Change PC331 from SE076104K80 S CER CAP .1U 16V K X7R to SE00000R700 S CER CAP 0.22U 16V K X7R 0402	2010/06/08	to PVT
15	BOM unique.	BOM unique.	0.1	38	Chnage PQ101 from SB301150000 to SB301150200.	2010/06/08	to PVT
16	EMI request.	EMI request.	0.1	38	Add PC1028 SE042104K80 S CER CAP .1U 25V K X7R 0603	2010/06/08	to PVT
17	EMI request.	EMI request.	0.1	44	Add PC1029 SE042104K80 S CER CAP .1U 25V K X7R 0603	2010/06/08	to PVT
18	Sourcer request to change a common part.	Sourcer request.	0.1	38	Change PC1019 from SE00000GC00 S CER CAP 2.2U 10V K X7R 0603 to SE000003H00 S CER CAP 2.2UF 10V K X5R 0603	2010/06/08	to PVT
19	Per sourcer request.	Sourcer request to change PC331 from SE00000R700 to SE026224K80 for common part.	0.1	43	change PC331 from SE00000R700 to SE026224K80	2010/06/08	to PVT
20	CPU transient issue.	Need modify PC362 to 330P due to transient fail.	0.1	45	Change PC362 from SE074561K80 S CER CAP 560P 50V K X7R 0402 to SE074331K80 S CER CAP 330P 50V K X7R 0402	2010/06/08	to PVT
21	Per sourcer request.	Per sourcer request.	0.1	43 44	Chnage PQ39/PQ82 from SB000008L80 to SB00000HL00.	2010/06/08	to PVT
22	Cost down.	Cost down 3VALWP and Charger Low Side MOS.	0.1	38 39	Change PQ98 from SB00000AJ00 to SB000009580(AO4468). Change PQ64 from SB00000CG00 to SB000009580(AO4468).	2010/06/08	to PVT
23	Cost down.	Cost down +1.5VP Low side MOS and choke.	0.1	42	Chnage PQ74 from SB000009F80 to SB00000AJ00(AO4712). Chnage PL32 from SH000009U00 to SH000009680.	2010/06/08	to PVT
24	Cost down.	re-caculate 1.5VP OCP.	0.1	42	Change PR417 from SD034110280 to SD034182280.	2010/06/08	to PVT

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Item	Fixed Issue	Reason for change	Rev.	PG#	Modify List	Date	Phase
24	Cost down.	re-caculate +3VALWP OCP.	0.1	38	Change PR562 from SD034107380 to SD034137380.	2010/06/08	to PVT
25	Cost down.	Sourcer request.	0.2	42	Change PU22 from SA000031D00 to SA00003HM00.	2010/07/07	to Pre-MP
26	Cost down.	Sourcer request.	0.2	43	Change PU999 from SA000031D00 to SA00003HM00.	2010/07/07	to Pre-MP
27	Pre-charge updated.	Pre-charge loss update, PVT use MEMO control.	0.2	39	Change PQ51 and PQ53 from SB00000DL00 to SB00000DJ10	2010/07/07	to Pre-MP
28	PVT B/I test hang up issue.	Because PVT has happened B/I hang up issue, that is because GFX_COREP drop to 100mV. Vendor is checking.	0.2	38	Change PQ102 from SB301150200 to SB000006800	2010/07/07	to Pre-MP
29	PVT B/I test hang up issue.	Because PVT has happened B/I hang up issue, that is because GFX_COREP drop to 100mV. Vendor is checking.	0.2	44	Chnage PU12 from SA00003XI00 to SA000030200.	2010/07/07	to Pre-MP
30	PVT B/I test hang up issue.	Because PVT has happened B/I hang up issue, that is because GFX_COREP drop to 100mV. Vendor is checking.	0.2	44	Change PR265 from SD034750180 to SD034226280.	2010/07/07	to Pre-MP
31	PVT B/I test hang up issue.	Because PVT has happened B/I hang up issue, that is because GFX_COREP drop to 100mV. Vendor is checking.	0.2	44	Change PR283 from SD000009380 to SD00000JB80	2010/07/07	to Pre-MP
32	PVT B/I test hang up issue.	Because PVT has happened B/I hang up issue, that is because GFX_COREP drop to 100mV. Vendor is checking.	0.2	44	Add PR288 SD034825A80.	2010/07/07	to Pre-MP
33	PVT B/I test hang up issue.	Because PVT has happened B/I hang up issue, that is because GFX_COREP drop to 100mV. Vendor is checking.	0.2	44	Add PC204 SE076103K80.	2010/07/07	to Pre-MP
34	Adjust +1.05VS_VTTP voltage.	Adjust +1.05VS_VTTP voltage by HW request.	0.2	44	Change PR271 from SD034287180 to SD034866180.	2010/07/07	to Pre-MP
35	Adjust +1.8V voltage.	Adjust +1.8V voltage by HW request.	0.2	43	Change PR476 from SD034590180 to SD034576180.	2010/07/07	to Pre-MP
36	Single source issue.	A04468 is single source, can't accept.	0.2	41	Change PR405 from SD034499180 to SD034487100.	2010/07/07	to Pre-MP
37	Adjust 3.3VALWP.	HW request to increase +3.3VALWP for camera voltage drop issue.	0.3	38	Change PQ98 and PQ64 from SB00009580 to SB0000AJ00.	2010/07/08	to Pre-MP
38			0.3	38	Change PR558 from SD034130280 to SD034137280.	2010/07/08	to Pre-MP
39							
40							
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Version change list (P. I. R. List)

Item	Phase	PAGE	DATE	Modifycatio list	Purpose
1	EVT		04 / 08 04 / 16 04 / 21 04 / 26 04 / 27 04 / 29 04 / 29 05 / 03 05 / 03 05 / 06 05 / 07 05 / 10 05 / 11 05 / 12 05 / 13	N/A Modify RTC SCH Change LAN to AR8152 Update Power SCH Update AND Gate symbol - U1 / U2 / U6 / U7 / U19 Update Power MOS symbol - U24 / U25 / U26 C196 change to 1U for INTEL design CLK GEN del C35 / C36 / C30 / C31 / C32 / C33 / C34 / L3 (for 3G@ and @ function) Q4+Q6 change to 2N7002DW Update U8 symbol Add DMIC function Modify USB define. Update Power SCH Add R17 for DMIC power Update Power SCH Change LAN to GIGA&10/100 co-lay Del R307 (LVDS conn.) Change +1.05VS_VTT to +1.05VS (CLK GEN) Change C842 / C850 / C854 / C656 to SE080105K80 1U_0603_10V6K Change R940 to 28K (S3 Power sequence) SW T1 pin define (LAN) Change R907 (0_0603) to 0_1206 5% Remove C40 / C41 / C42 / C43 / C44 Change FAN Conn. Update Power SCH Change LAN to BCM57780 GIGA Del R637 / R639 / R640 / R645 / R647 / R648 (AUDIO) Del DMIC Update Power SCH Update Power SCH Add C49 / C50 (EC B+) Update Power SCH C764 / C765 change to 18P_0402_50V8J (EC) Reserve D13 / D24 / D30 (ESD) U17 change to RTS5137 (SA000043500) Card reader Update Power SCH	
			06 / 01 06 / 02 06 / 03 06 / 04 06 / 10	Unpop L11 / C247 & Pop R304 for cost down Unpop C764 / X2 & Pop R13 & C765 Change to 100K for EC remove Crystal Pop R834 & R835 change to 200K for Project 1D Unpop D24 / D30 Unpop R690 / R691 Pop C49 / C50 For HDMI Unpop R753 / R757 , Add R754 / R758 to 0 ohm. Change R759 to 3.9K. Change R755 to 4.7K ohm , Unpop R748. Unpop R778. R833 / R292 0_0805 change to 0_0603. C190 / C191 & C1135 / C1136 change to 33P 0402 50V8J for Vender test report Unpop C774-C799 for EMI cost down. Update Power SCH Add T25 / T26 / T27	
			07 / 07 07 / 08	Update Power SCH Change R759 to 4.32K_1% SD00000J280	

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